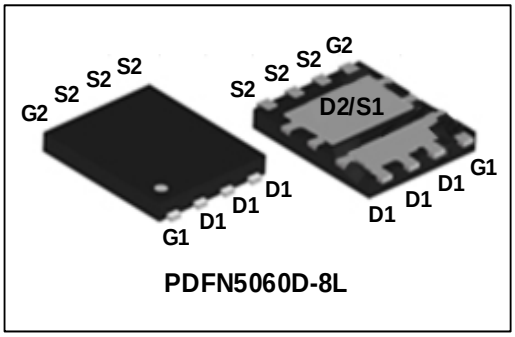
WMB0305BDG

30V Dual N-Channel Enhancement Mode Power MOSFET

Description

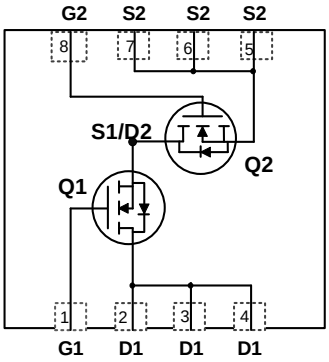
The WMB0305BDG is a high performance trench Dual N-channel asymmetric MOSFET which utilizes extremely high cell density to provide low Rdson and gate charge characteristics. It is ideally suited to support synchronous buck converter applications.



Features

- Q1: V_{DS} = 30V, I_D = 58A
  Typ. $R_{DS(on)}$ = 4.4mΩ @ V_{GS} = 10V
  Typ. $R_{DS(on)}$ = 5.9mΩ @ V_{GS} = 4.5V
- Q2: V_{DS} = 30V, I_D = 120A
  Typ. $R_{DS(on)}$ = 2.2mΩ @ V_{GS} = 10V
  Typ. $R_{DS(on)}$ = 3.0mΩ @ V_{GS} = 4.5V
- Dual Asymmetric N-Channel
- Green Device Available
- 100% EAS Guaranteed
- RoHS Compliant & Halogen-Free

RoHS compliant



Applications

- Optimized for High Performance Buck Converters
- CCFL Back-light Inverter

Absolute Maximum Ratings（Tc = 25°C, unless otherwise noted）

Parameter		Symbol	Value		Unit
			Q1	Q2	
Drain-Source Voltage		V_{DS}	30	30	V
Gate-Source Voltage		V_{GS}	±20	±20	V
Continuous Drain Current	T_C=25°C	I_D	58	120	A
	T_C=100°C		37	75.6	
Pulsed Drain Current[1]		I_{DM}	232	480	A
Single Pulse Avalanche Energy[2]		EAS	45	115.2	mJ
Total Power Dissipation	T_C=25°C	P_D	29.8	59.5	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value		Unit
		Q1	Q2	
Thermal Resistance from Junction-to-Ambient[3]	$R_{\theta JA}$	60	55	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	4.2	2.1	

Q1-Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	30	-	-	V
Gate-body Leakage Current		I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T_J=25°C	I_{DSS}	V_{DS} = 30V, V_{GS} = 0V	-	-	1	μA
	T_J=100°C			-	-	100	
Gate-Threshold Voltage		$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	1.7	2.2	V
Drain-Source On-Resistance[4]		$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A	-	4.4	5.5	mΩ
			V_{GS} = 4.5V, I_D =10A	-	5.9	7.8	
Forward Transconductance[4]		g_{fs}	V_{DS} = 10V, I_D = 20A	-	130	-	S
Dynamic Characteristics[5]							
Input Capacitance		C_{iss}	V_{DS} = 15V, V_{GS} =0V, f =1MHz	-	885	-	pF
Output Capacitance		C_{oss}		-	343	-	
Reverse Transfer Capacitance		C_{rss}		-	31	-	
Gate Resistance		R_g	f = 1MHz	-	1.9	-	Ω
Switching Characteristics[5]							
Total Gate Charge		Q_g	V_{GS} = 10V, V_{DS} = 15V, I_D= 20A	-	15	-	nC
Gate-Source Charge		Q_{gs}		-	2.8	-	
Gate-Drain Charge		Q_{gd}		-	3.1	-	
Turn-On Delay Time		$t_{d(on)}$	V_{GS} =10V, V_{DD} = 15V, R_G = 3Ω, I_D= 20A	-	6.1	-	ns
Rise Time		t_r		-	3	-	
Turn-Off Delay Time		$t_{d(off)}$		-	16.8	-	
Fall Time		t_f		-	3.3	-	
Body Diode Reverse Recovery Time		t_{rr}	I_F = 20A, di/dt = 100A/μs	-	10	-	ns
Body Diode Reverse Recovery Charge		Q_{rr}		-	13.2	-	nC
Drain-Source Body Diode Characteristics							
Diode Forward Voltage[4]		V_{SD}	I_S = 20A, V_{GS} = 0V	-	-	1.2	V
Continuous Source Current	T_C=25°C	I_S	-	-	-	58	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
2. The test condition is V_{DD}=25V,V_{GS}=10V,L=0.4mH，I_{AS}=15A.
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

Q1-Typical Characteristics

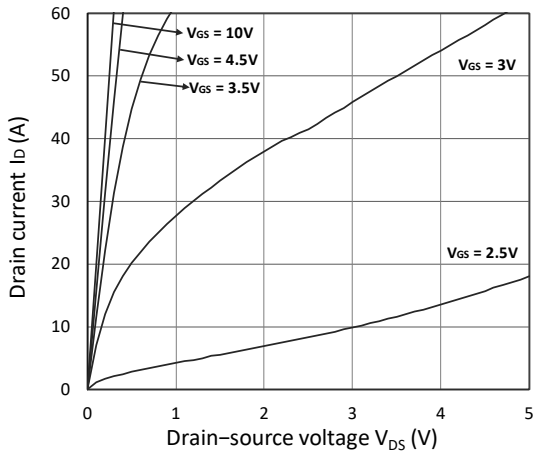


Figure 1. Output Characteristics

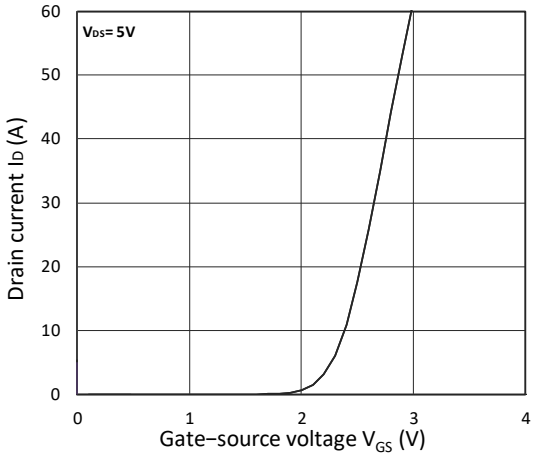


Figure 2. Transfer Characteristics

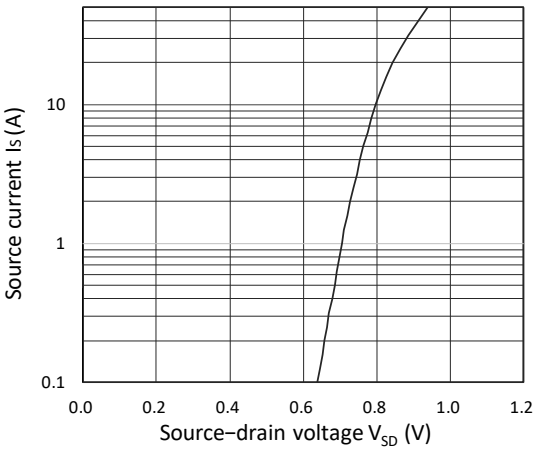


Figure 3. Forward Characteristics of Reverse

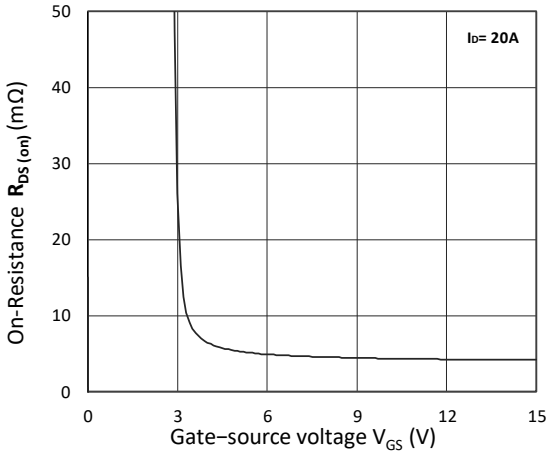


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

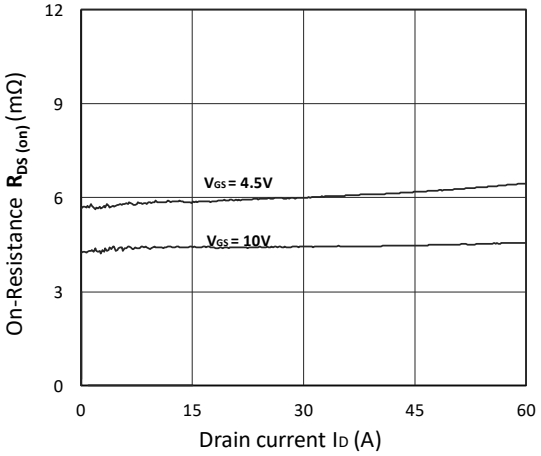


Figure 5. $R_{DS(ON)}$ vs. I_D

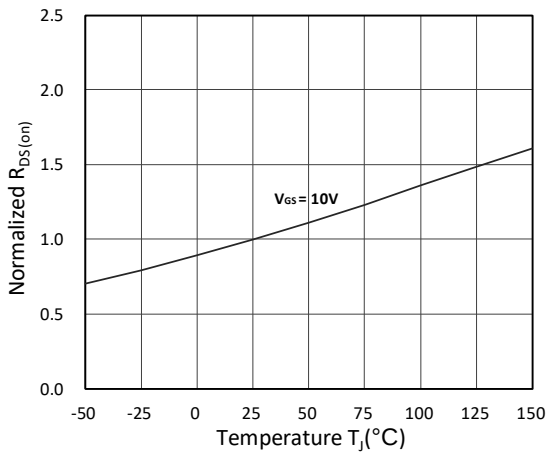


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

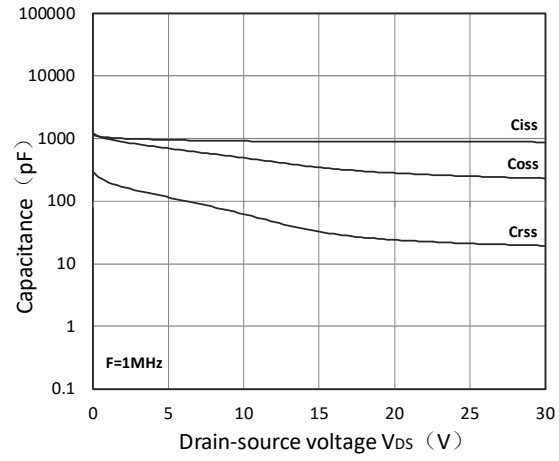


Figure 7. Capacitance Characteristics

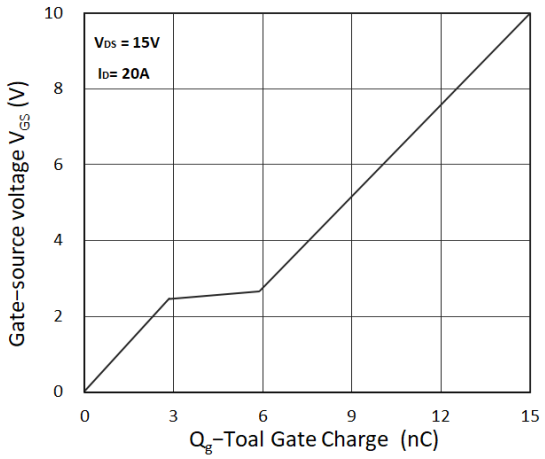


Figure 8. Gate Charge Characteristics

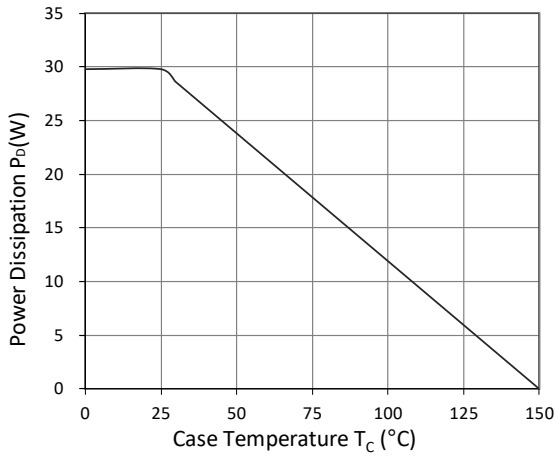


Figure 9. Power Dissipation

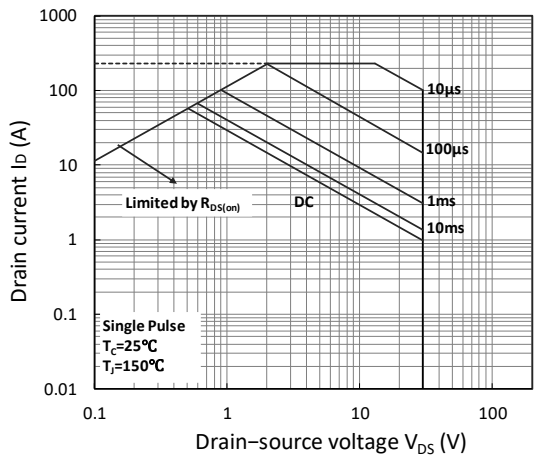


Figure10. Safe Operating Area

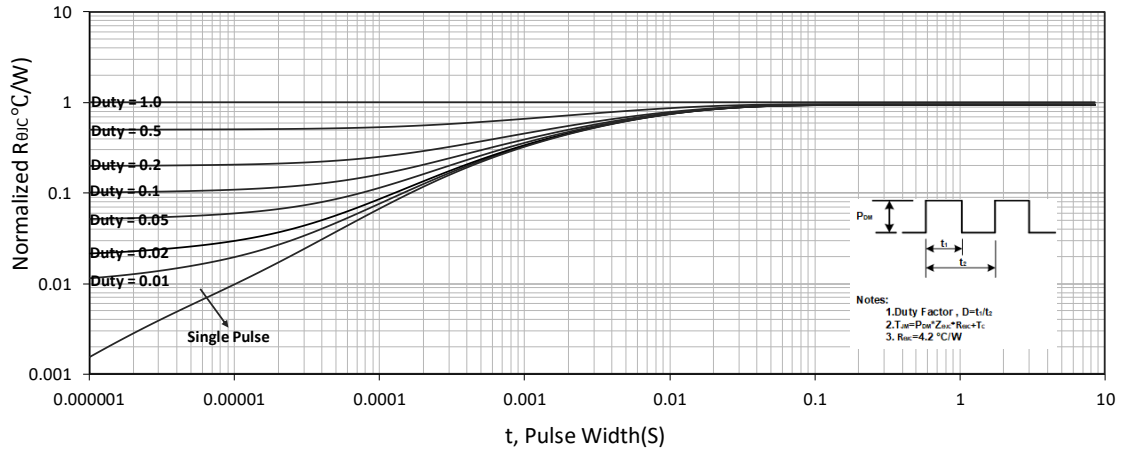


Figure 11. Normalized Maximum Transient Thermal Impedance

Q2-Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	30	-	-	V
Gate-Body Leakage Current		I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T_J=25°C	I_{DSS}	V_{DS} = 30V, V_{GS} = 0V	-	-	1	μA
	T_J=100°C			-	-	100	
Gate-Threshold Voltage		$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	1.6	2.2	V
Drain-Source on-Resistance[4]		$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A	-	2.2	2.6	mΩ
			V_{GS} = 4.5V, I_D = 10A	-	3	3.8	
Forward Transconductance[4]		g_{fs}	V_{DS} = 10V, I_D = 20A	-	120	-	S
Dynamic Characteristics[5]							
Input Capacitance		C_{iss}	V_{DS} = 15V, V_{GS} =0V, f =1MHz	-	2080	-	pF
Output Capacitance		C_{oss}		-	768	-	
Reverse Transfer Capacitance		C_{rss}		-	66	-	
Gate Resistance		R_G	f =1MHz	-	2	-	Ω
Switching Characteristics[5]							
Total Gate Charge		Q_g	V_{GS} = 10V, V_{DS} = 15V, I_D= 20A	-	35	-	nC
Gate-Source Charge		Q_{gs}		-	5	-	
Gate-Drain Charge		Q_{gd}		-	5.5	-	
Turn-on Delay Time		$t_{d(on)}$	V_{GS} =10V, V_{DD}= 15V, R_G = 3Ω, I_D= 20A	-	7.7	-	ns
Rise Time		t_r		-	5.5	-	
Turn-off Delay Time		$t_{d(off)}$		-	32.5	-	
Fall Time		t_f		-	9.3	-	
Body Diode Reverse Recovery Time		t_{rr}	I_F = 20A, di/dt = 100A/μs	-	42	-	ns
Body Diode Reverse Recovery Charge		Q_{rr}		-	16.8	-	nC
Drain-Source Body Diode Characteristics							
Diode Forward Voltage[4]		V_{SD}	I_S = 20A, V_{GS} = 0V	-	-	1.2	V
Continuous Source Current	T_C=25°C	I_S	-	-	-	120	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
2. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.4mH, I_{AS}=24A.
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

Q2-Typical Characteristics

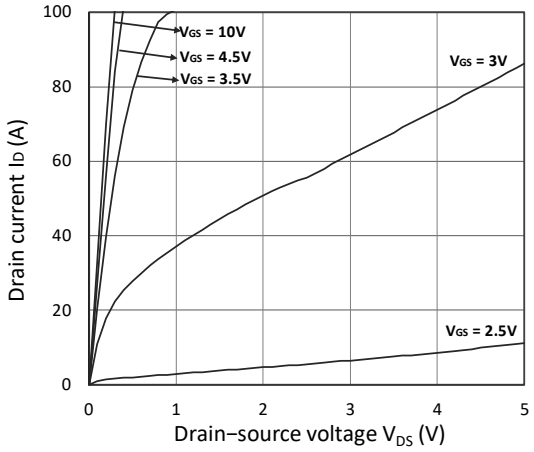


Figure 1. Output Characteristics

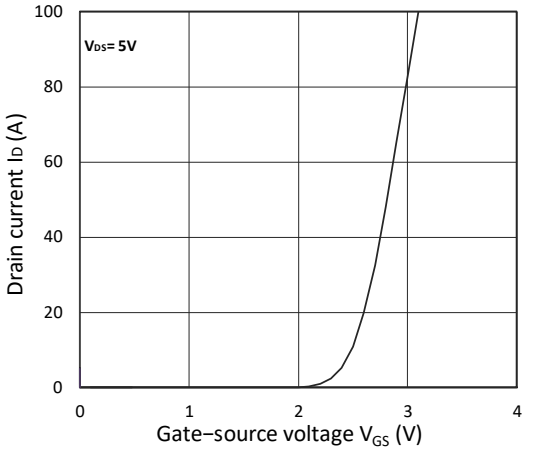


Figure 2. Transfer Characteristics

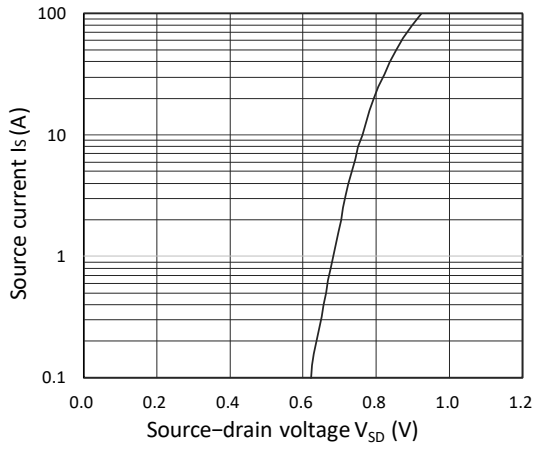


Figure 3. Forward Characteristics of Reverse

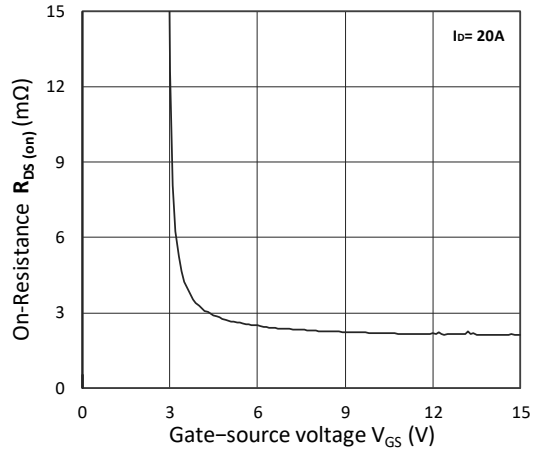


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

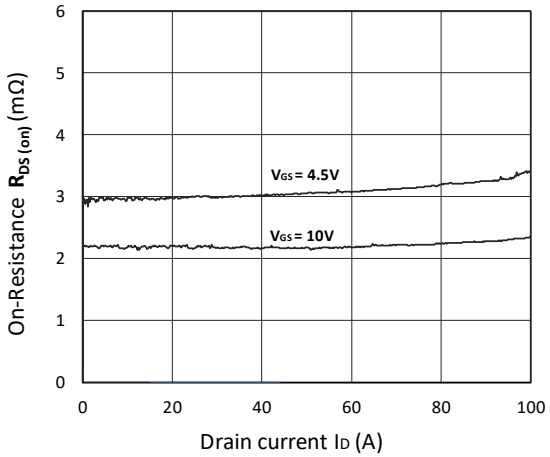


Figure 5. $R_{DS(ON)}$ vs. I_D

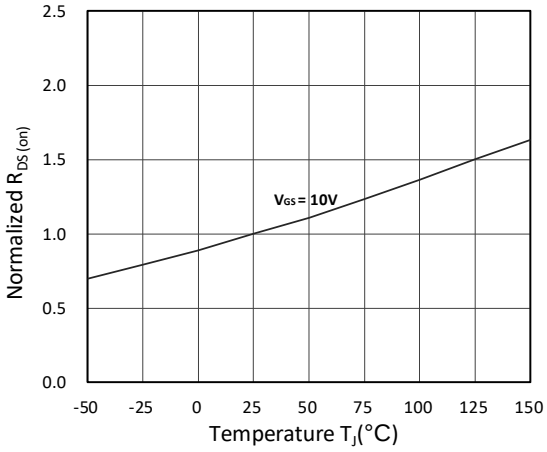


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

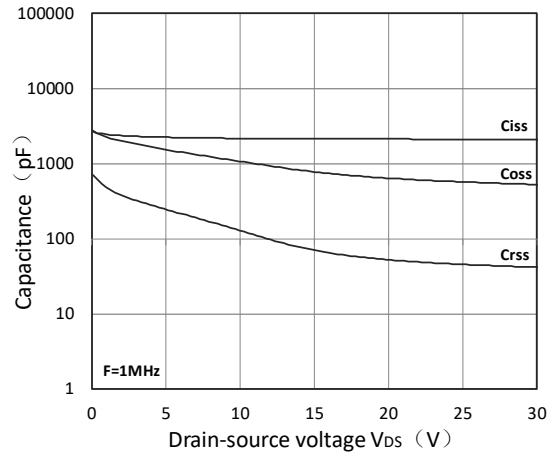


Figure 7. Capacitance Characteristics

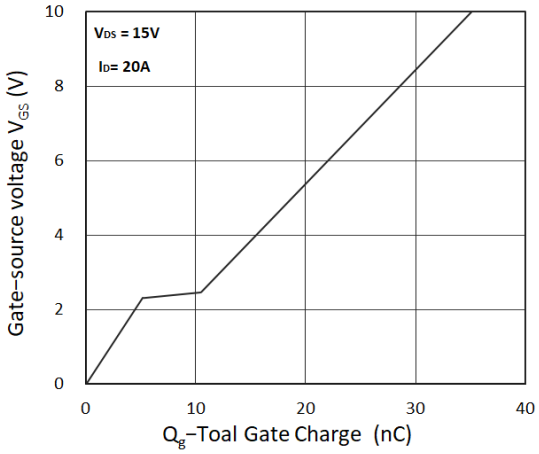


Figure 8. Gate Charge Characteristics

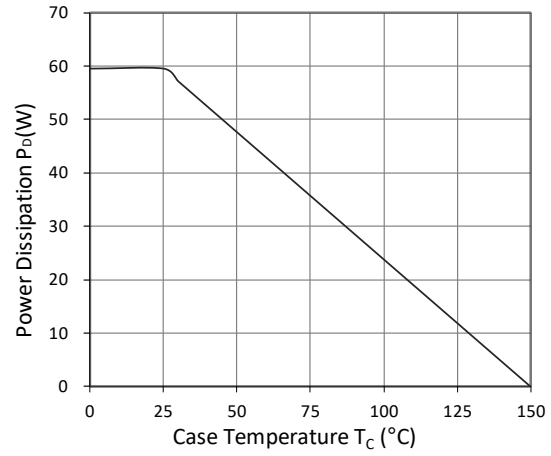


Figure 9. Power Dissipation

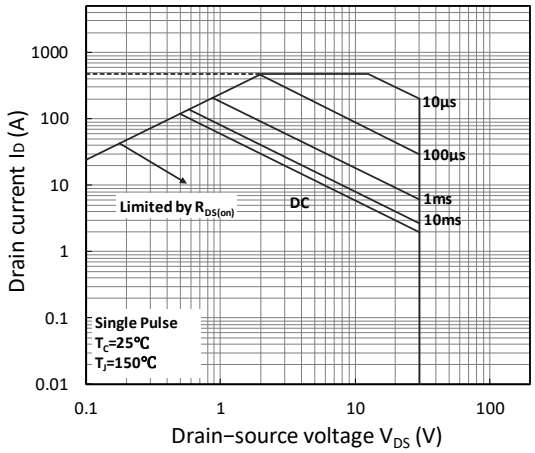


Figure10. Safe Operating Area

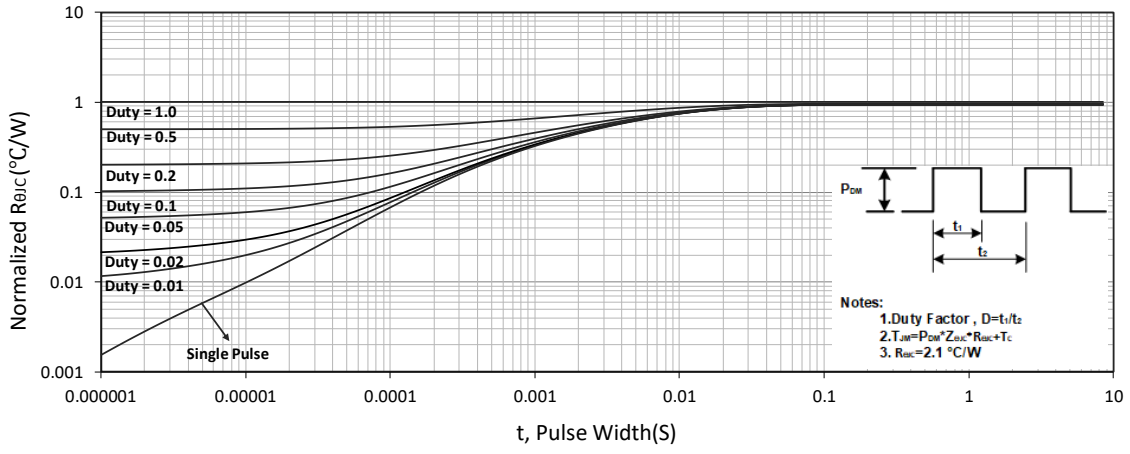


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

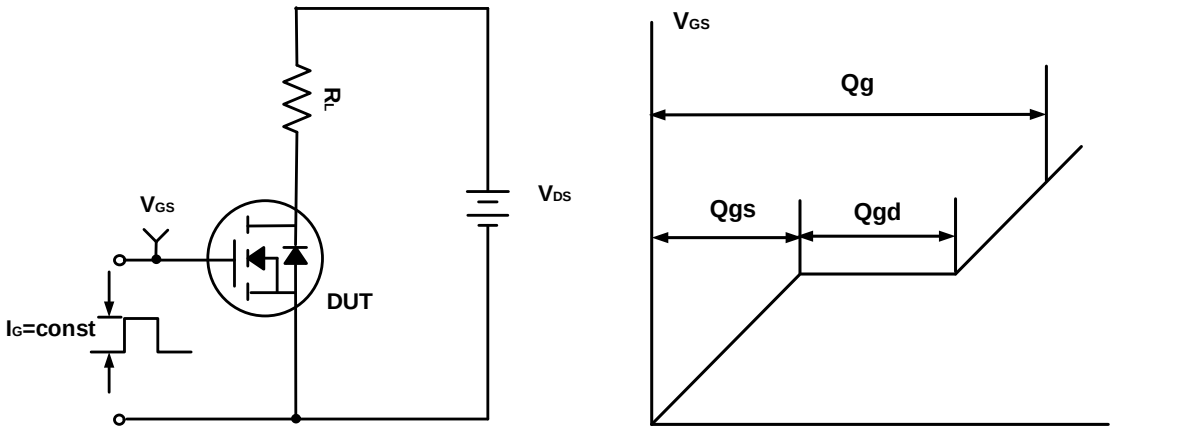


Figure A. Gate Charge Test Circuit & Waveforms

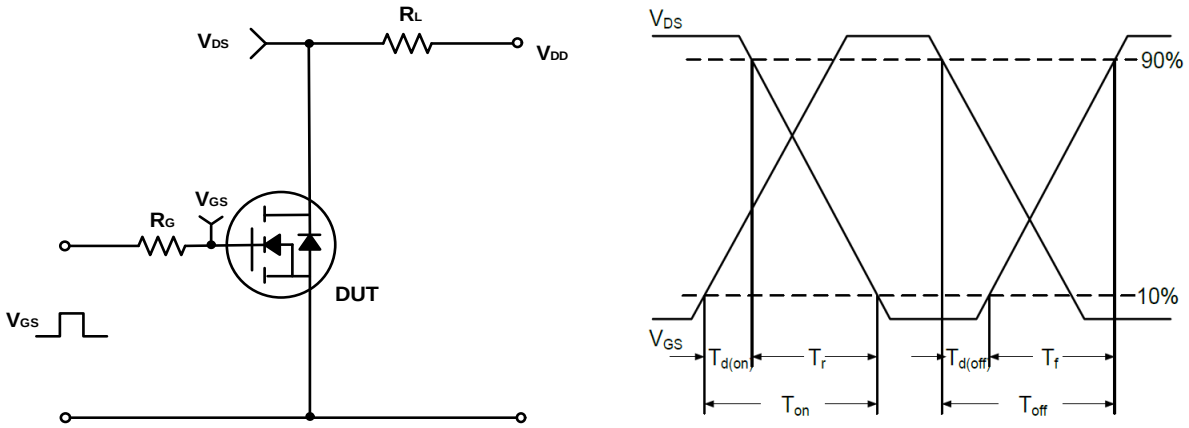


Figure B. Switching Test Circuit & Waveforms

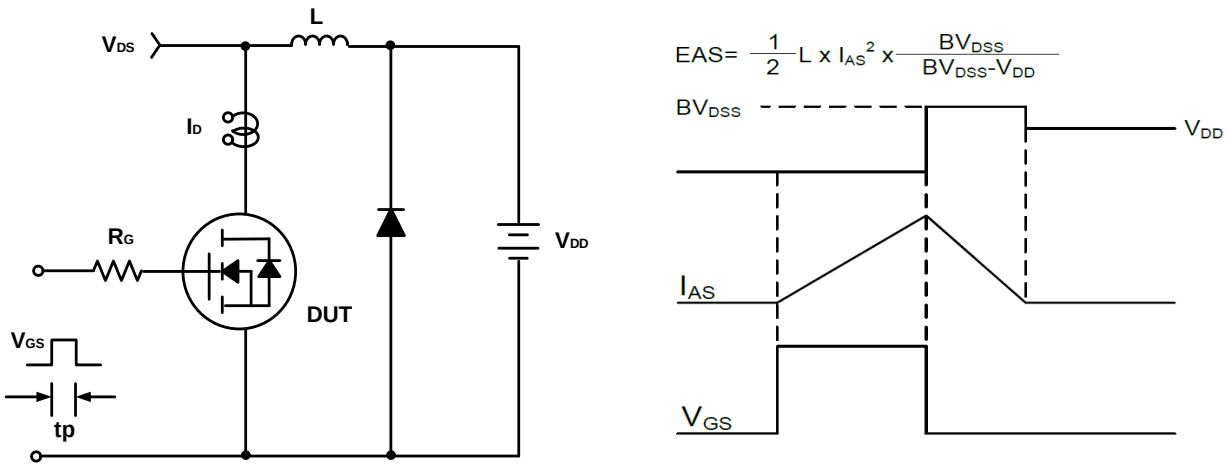
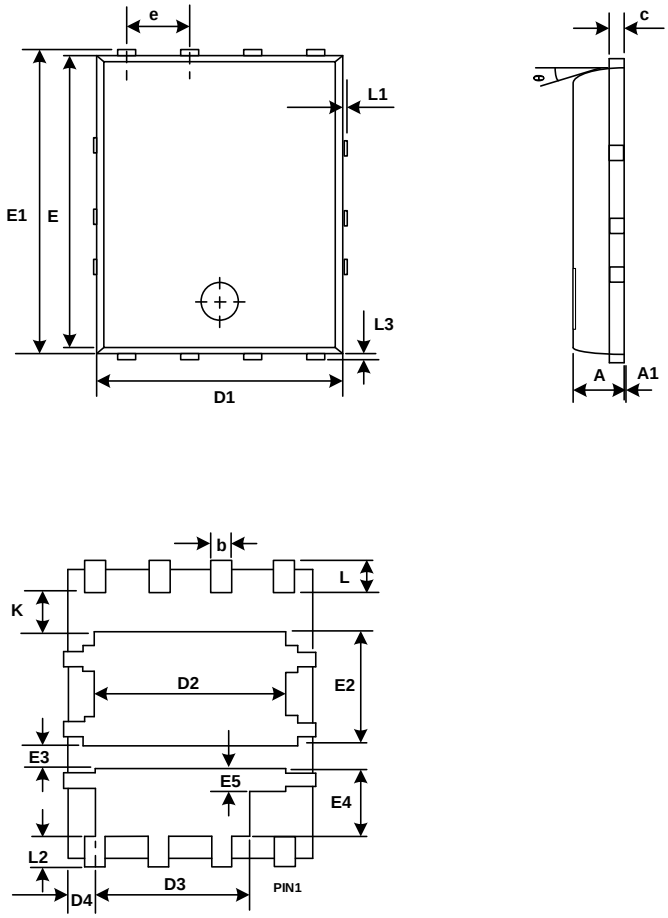


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for Dual PDFN5060D-8L



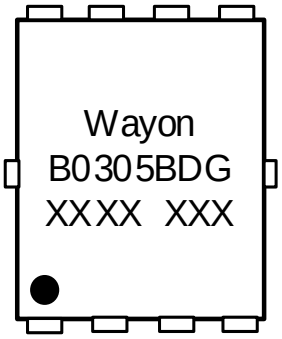
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.90	1.10
A1	0.00	0.05
b	0.30	0.51
C	0.20	0.30
D1	4.80	5.00
D2	3.60	4.00
D3	2.85	3.25
D4	0.45	0.65
E	5.70	5.80
E1	5.90	6.20
E2	2.02	2.32
E3	0.40	0.60
E4	1.22	1.42
E5	0.31	0.51
e	1.27BSC	
L	0.50	0.71
L1	0.00	0.20
L2	0.48	0.68
L3	0.05	0.25
K	0.50	1.00
θ	0°	12°

Ordering Information

Part	Package	Marking	Packing method
WMB0305BDG	PDFN5060D-8L	B0305BDG	Tape and Reel

Marking Information



B0305BDG = Device code

XXXX XXX= Date code

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207
Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com
WAYON website: **http://www.way-on.com**
For additional information, please contact your local Sales Representative.

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Product Specification Statement

1. The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.

2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.

3. WAYON strives to provide accurate and up-to-date information to the best of our ability. However, due to technical, human, or other reasons, WAYON cannot guarantee that the information provided in the product specification is entirely accurate and error-free. WAYON shall not be held responsible for any losses or damages resulting from the use or reliance on any information in these product specifications. WAYON reserves the right to revise or update the product specification and the products at any time without prior notice, and the user's continued use of the product specification is considered an acceptance of these revisions and updates. Prior to purchasing and using the product, users should verify the above information with WAYON to ensure that the product specification is the most current, effective, and complete. If users are particularly concerned about product parameters, please consult WAYON in detail or request relevant product test reports. Any data not explicitly mentioned in the product specification shall be subject to separate agreement.

4. Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.

5. The design of the product is intended to meet civilian needs and is not guaranteed for use in harsh environments or precision equipment. It is not recommended for use in systems or equipment such as medical devices, aircraft, nuclear power, and similar systems, where failures in these systems or equipment could reasonably be expected to result in personal injury. WAYON shall assume no responsibility for any consequences resulting from such usage.

6. Users should also comply with relevant laws, regulations, policies, and standards when using the product specification. Users are responsible for the risks and liabilities arising from the use of the product specification and must ensure that it is not used for illegal purposes. Additionally, users should respect the intellectual property rights related to the product specification and refrain from infringing upon any third-party legal rights. WAYON shall assume no responsibility for any disputes or controversies arising from the above-mentioned issues in any form.