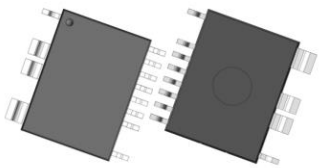
WPM4DT602SUC2

600V 4A half-bridge Integrated Power Modules

Description

WPM4DT602SUC2 is half-bridge Modules (IPM)designed for advanced appliance motor driveapplications such as energy efficient fans and pumps.These modules Integrated 2 MOSFET and 1 highvoltage gate driver in a familiar package.



SOICW16-12

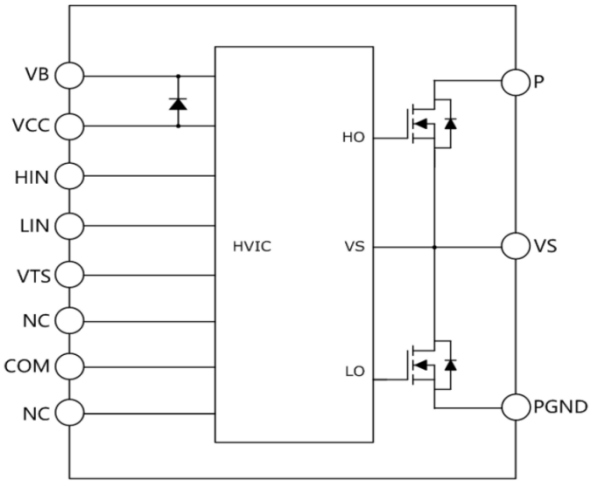
Features

- 600V half-bridge including high voltage gate drivers
- Works with 3.3V/5V MCU, Active-High interface
- Integrated bootstrap functionality
- HVIC for Under-voltage Protection
- HVIC Temperature-Sensing Built-In for temperature Monitoring

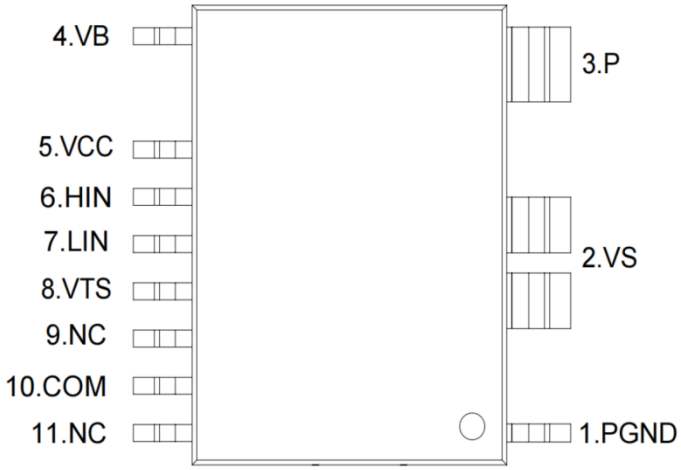
Applications

- Energy efficient fans
- Pumps
- High speed hair dryer

Internal Electrical Schematic



Module Pin-Out Description



Pin Number	Pin Name	Pin Description
1	PGND	Source for low side MOSFET
2	VS	High Side Floating Offset Voltage
3	P	DC Bus Voltage Positive
4	VB	High Side Floating Supply Voltage
5	VCC	Power Supply for Low Side
6	HIN	Logic Input for High Side Gate Driver
7	LIN	Logic Input for Low Side Gate Driver
8	VTS	Temperature-sensing Voltage Output
9	NC	Not Connected
10	COM	Logic Ground
NC	NC	Not Connected

● Absolute Maximum Ratings（T_C=25℃ unless otherwise specified）

Inverter Part

Parameter	Symbol	Condition	Ratings	Unit
Drain-Source Voltage of Each MOSFET	V_{DSS}	-	600	V
Each MOSFET Current, Continuous	I_D	T_c=25℃ (Silicon limit)	4	A
Each MOSFET Current, Peak	I_{DP}	T_c=25℃ Pulse width less than 100us	TBD	A
Maximum Power Dissipation	P_D	T_c=25℃ For Each MOSFET	TBD	W

Control Part

Parameter	Symbol	Condition	Ratings	Unit
Control Supply Voltage	V_{CC}	Applied between V_{CC} - COM	20	V
High-side Bias Voltage	V_{BS}	T_c=25℃ (Silicon limit)	20	V
Input Signal Voltage	V_{IN}	T_c=25℃ Pulse width less than 100us	-0.3~V_{CC}+0.3	V

Bootstrap Diode Part

Parameter	Symbol	Condition	Ratings	Unit
Bootstrap Diode Reverse Voltage	V_{RRMB}	-	600	V

System

Parameter	Symbol	Condition	Ratings	Unit
Operating Junction Temperature	T_j	-	-20~+150	℃
Storage temperature	T_{STG}	-	-20~+125	℃

Thermal Resistance

Parameter	Symbol	Condition	Ratings	Unit
Junction to Case Thermal Resistance	Rth(j-c)	For Each MOSFET	TBD	K/W

● Recommended Operating Conditions（T_C=25℃）

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	V_{PN}	Applied between P – N	-	300	400	V
Control Supply Voltage	V_{CC}	Applied between V_{CC} – COM	13.5	15.0	16.5	V
High side Supply Voltage	V_{BS}	Applied between V_B – V_S	13.5	15.0	16.5	V
Dead time	t_{dead}	For each input signal	1	-	-	μs
PWM input frequency	f_{PWM}	V_{CC} = V_{BS} = 13.5~16.5	-	20	-	kHz

● Electrical Characteristics （T_C=25℃）

Inverter Part

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{IN} = 0V , I_D =250uA (NOTE 2)	600	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{IN} = 0V , V_{DS} = 600V	-	-	10	μA
Drain-Source Diode Forward Voltage	V_{SD}	V_{CC} = V_{BS}= 15V,V_{IN} = 0V,I_D = -4A	-	-	1.4	V
Drain-Source Turn-On Resistance	R_{DSON}	V_{CC} = V_{BS}= 15V,V_{IN} = 5V,I_D = 2A	-	2.0	-	Ω
Switching time	t_{on}	V_D = 300V, V_{CC} = V_{BS} = 15V , I_D =5A , V_{IN} = 0V—5V, Inductive Load (NOTE 3)	-	800	-	ns
	t_{off}		-	600	-	ns
	t_{rr}		-	120	-	ns
	E_{on}		-	180	-	μJ
	E_{off}		-	10	-	μJ

NOTE 2: BVDSS is the absolute maximum voltage rating between drain and source terminal of each MOSFET inside IPM. V_{PN} should be sufficiently less than this value considering the effect of the stray inductance so that V_{DS} should not exceed BV_{DSS} in any case.

NOTE 3: t_{ON} and t_{OFF} include the internal transmission delay time of the driver IC. Please refer to Figure 1 for details.

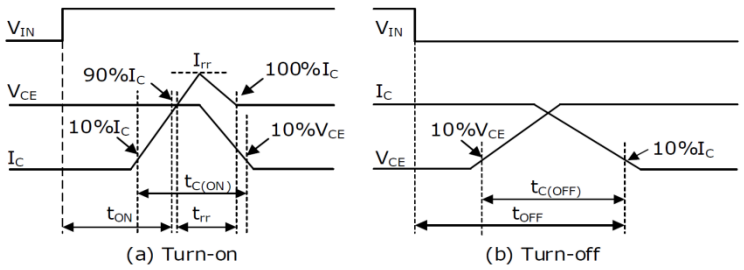


Figure 1: Switching Time Definition

Control Part

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
V_{CC} Quiescent Current	I_{QCC}	V_{CC} = 15V ,V_{IN} = 0V	-	270	400	μA
V_{BS} Quiescent Current	I_{QBS}	V_{BS} = 15V ,V_{IN} = 0V	-	75	160	μA
Input current	I_{IN}	V_{IN} = 5V	-	10	-	μA
Low-side under-voltage protection(UV)	UV_{CCD}	Detection level	8.0	8.8	9.6	V
	UV_{CCR}	Reset level	8.4	9.2	10.0	V
High-side under-voltage protection(UV)	UV_{BSD}	Detection level	8.0	8.8	9.6	V
	UV_{BSR}	Reset level	8.4	9.2	10.0	V
Turn on threshold voltage	V_{IH}	Applied between V_{IN} - COM	-	-	3.0	V
Turn off threshold voltage	V_{IL}		1.5	-	-	V
Temperature output	V_{TS}	T_{HVIC} = 25°C	0.64	0.89	1.14	V
		T_{HVIC} = 100°C	2.00	2.26	2.51	V

Bootstrap Diode Part

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V_{FB}	I_F = 10mA , T_C = 25℃	-	3.0	-	V
Bootstrap emulator on resistance	R_{BSD}	V_{CC} = 15V, T_C = 25℃	-	240	-	Ω

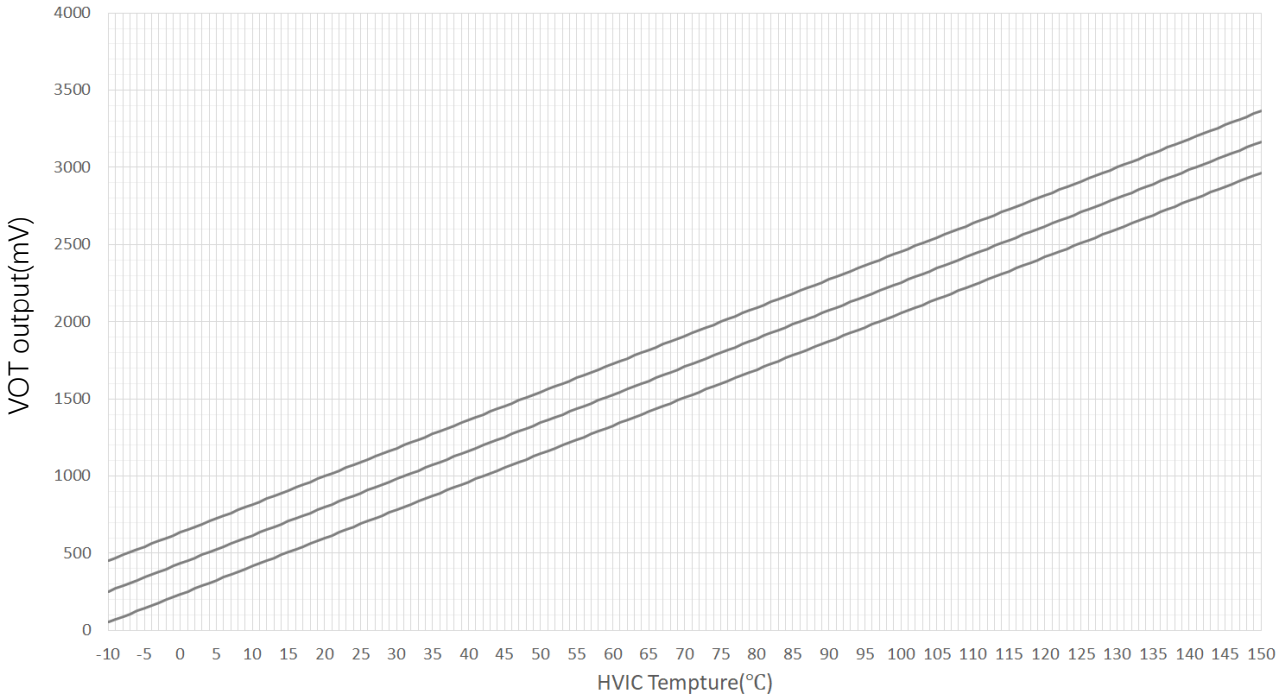
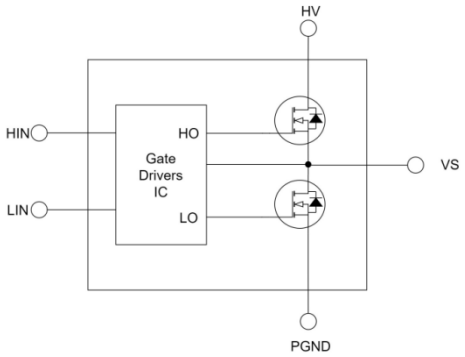


Figure 2: VTS output voltage curve

NOTE: $V_{VTS}(mV)=18.2xT_{HVIC}+435\pm250$

Application Guide

Bootstrap Diode Part



HIN	LIN	HO	LO	Note
1	0	1	0	High Side MOS open,Low Side MOS close
0	1	0	1	High Side MOS close,Low Side MOS open
0	0	0	0	High Side MOS close,Low Side MOS close
1	1	0	0	High Side MOS close,Low Side MOS close

Timing diagram of protection function

[Under-Voltage Protection] Low-side

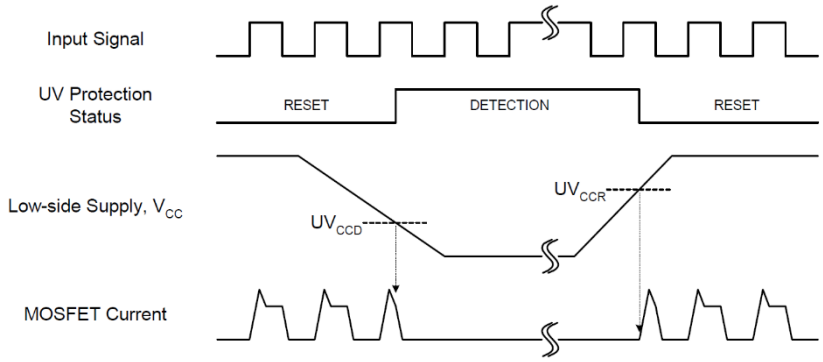


Figure 3: Timing diagram of under-voltage protection (Low-side)

[Under-Voltage Protection] High-side

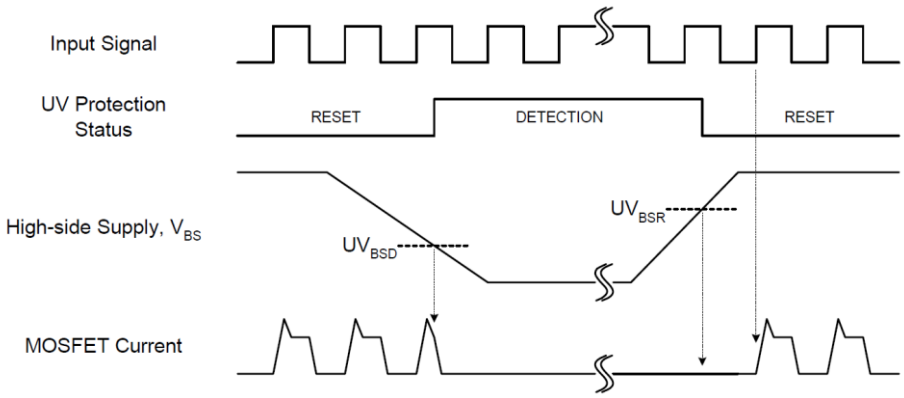


Figure 4: Timing diagram of under-voltage protection (High-side)

● Example of Application Circuit

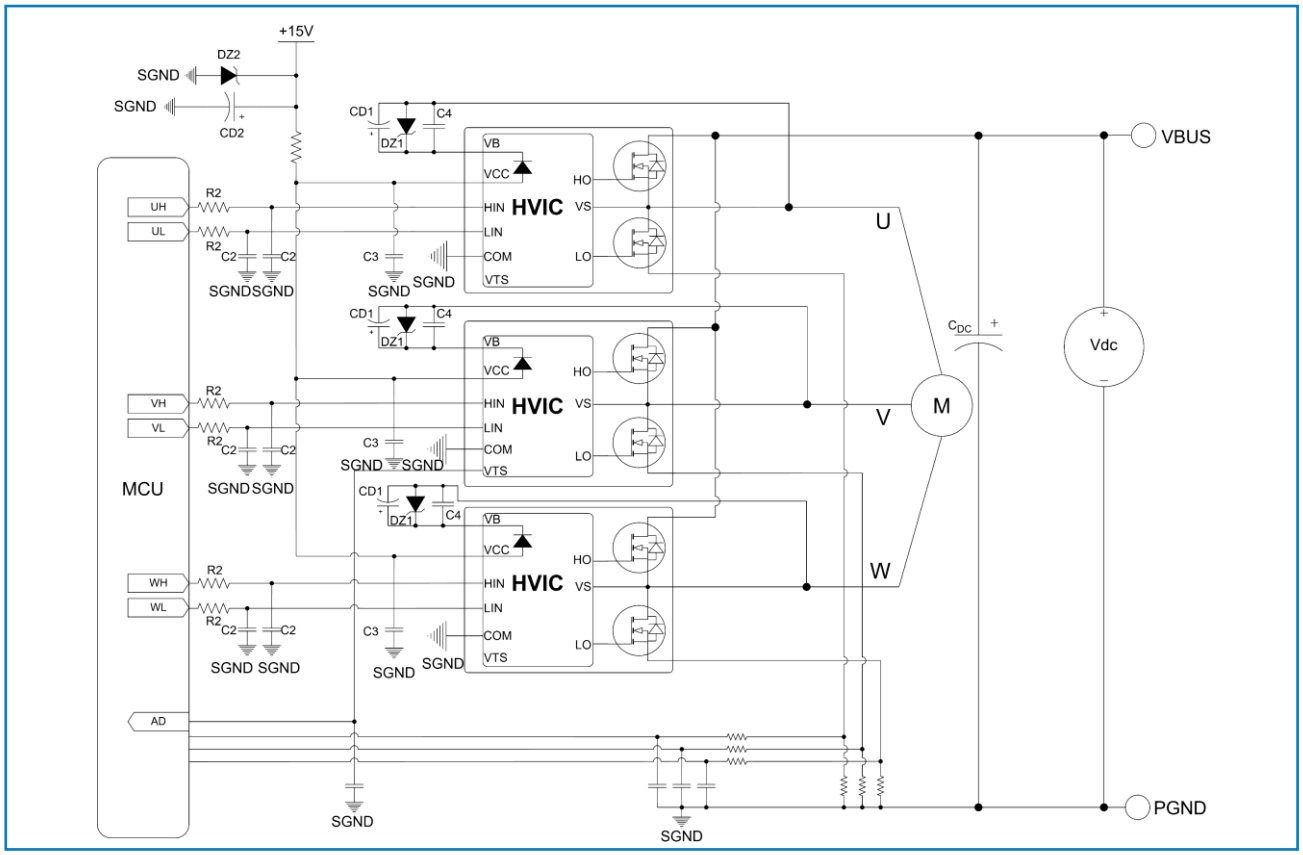


Figure 5: Application Circuit

Notes：

1、To avoid input signal oscillation, the wiring of each input should be as short as possible, and the use of RC filters (R2, C2) on each input signal is recommended. The filters should be with a time constant of about 100 ns and placed as close as possible to the IPM input pins.

2、The use of a bypass capacitor CD2 (aluminum or tantalum) can reduce the transient circuit demand on the power supply. The recommended value of CD2 is range of 10uf or more. Also, to reduce any high-frequency switching noise distributed on the power lines, a decoupling capacitor C3 (100 to 220nF, with low ESR and low ESL) should be placed as close as possible to the VCC pin and in parallel with the bypass capacitor.

3、The decoupling capacitor C4 (from 100 to 220nF, ceramic with low ESR and low ESL), in parallel with each CD1, filters high-frequency disturbance. The recommended value of CD1 is between 10uF and 47uF. CD1 and C4 should be placed as close as possible to the SW and BST pins. Bootstrap negative electrodes should be connected to SW terminals directly and separated from the main output wires.

4、To avoid overvoltage on the VCC pin, a Zener diode (DZ2) can be used. Similarly, on the BST pin, a Zener diode (DZ1) can be placed in parallel with each CD1.

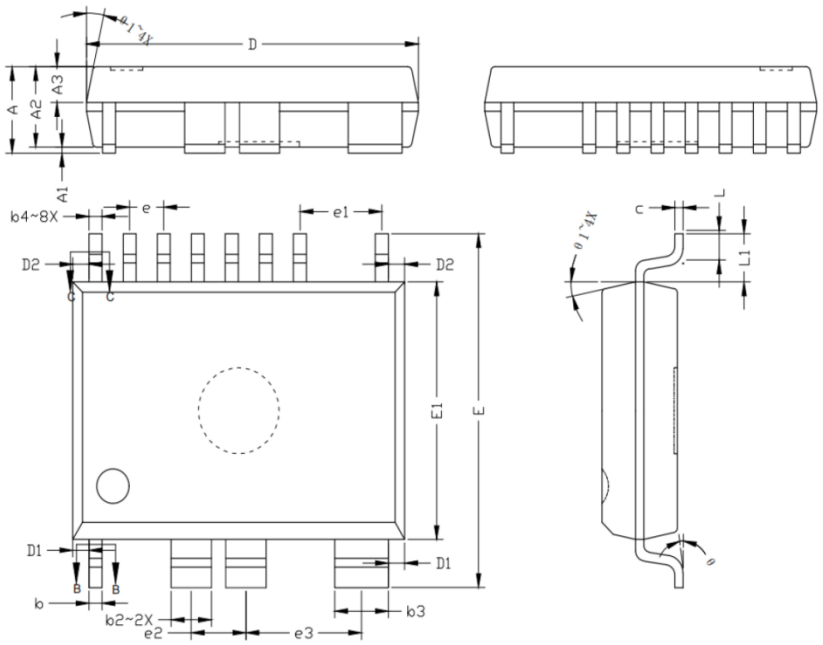
5、Low-inductance shunt resistors Rcs to be used for phase leg current sensing.

6、In order to avoid malfunctions, the wiring on vs pins, the shunt resistor and PGND should be as short as possible.

7、The connection of SGND to PGND on one point only (close to the shunt resistor terminal) can reduce the impact of power ground fluctuation.

Mechanical Dimensions for SOICW16-12

UNIT:mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	--	--	2.615
A1	0.125	--	0.225
A2	2.29	2.34	2.39
A3	0.993	1.043	1.093
b	0.37	--	0.47
b1	0.36	0.40	0.46
b2	--	--	1.25
b3	--	--	1.67
b4	0.35	--	0.45
b5	0.34	0.38	0.44
c	0.25	--	0.29
c1	0.24	0.25	0.26
D	10.20	10.30	10.40
D1	0.502(BSC)		
D2	0.515(BSC)		
E	10.106	10.306	10.506
E1	7.40	7.50	7.60
e	1.058(BSC)		
e1	2.54(BSC)		
e2	1.69(BSC)		
e3	3.60(BSC)		
L	0.75	--	0.97
L1	1.403(REF)		
θ	0°	~	8°
θ1	6°	~	15°

Ordering Information

Part	Package	Marking	QTY(PCS)	Packing method
WPM4DT602SUC2	SOICW16-12	WPM4DT602SUC2	TBD	Tube

Contact Information

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