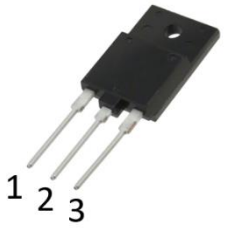
WAYON

T4050H-800EA

Silicon Controlled Rectifier

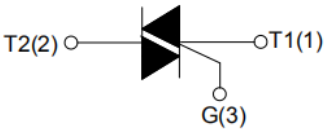
Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant
- High Dv/Dt Rate
- $I_{T(RMS)}$ to 40A of Triacs
- High Junction Temperature and High Environment Temperature Condition



TO-3PF

Pin Configuration



Absolute Maximum Ratings（Tc=25℃ Unless otherwise specified）

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40~150	℃
Operating junction temperature range	T_j	-40~150	℃
Repetitive peak off-state voltage (Tj=25℃)	V_{DRM}	800	V
Repetitive peak reverse voltage (Tj=25℃)	V_{RRM}	800	V
RMS on-state current	$I_{T(RMS)}$	40	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	400	A
I^2t value for fusing (tp=10ms)	I^2t	800	A^2s
Critical rate of rise of on-state current (IG=2×IGT)	dI/dt	50	A/μs
Peak gate current	I_{GM}	4	A
Average gate power dissipation	$P_{G(AV)}$	1	W

Peak gate power	P_{GM}	10	W
Thermal Resistance(between Junction and Case) @TO-3PF	$R_{\theta(J-C)}$	1.1（Typ.）	℃/W

Electronics Characteristics（Tc=25℃ Unless otherwise specified）

3 Quadrants：

Parameter	Symbol	Quadrant		Value	Unit
				T4050	
Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω	I_{GT}	Ⅰ-Ⅱ-Ⅲ	MAX	50	mA
Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω	V_{GT}			1.5	V
Gate non-trigger voltage@VD=VDRM	V_{GD}	Ⅰ-Ⅱ-Ⅲ	MIN	0.2	V
Holding Current@IT=500mA	I_H	-	MAX	60	mA
Latching Current@IG=1.2IGT	I_L	Ⅰ-Ⅲ	MAX	80	mA
		Ⅱ		100	
Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=125℃,Gate Open	dV/dt	-	MIN	500	V/μs
Peak Forward On-State Voltage @ITM=60A,tp=380μs, Tj=25℃	V_{TM}	-	MAX	1.55	V
Peak Repetitive Forward @VDRM=VRRM,Tj=25℃	I_{DRM}	-	MAX	10	μA
Reverse Blocking Current @VDRM=VRRM,Tj=125℃	I_{RRM}	-	MAX	5	mA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

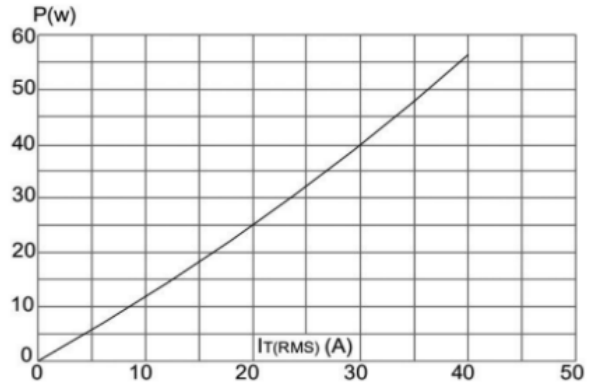


FIG.2: RMS on-state current versus case temperature in different packaging

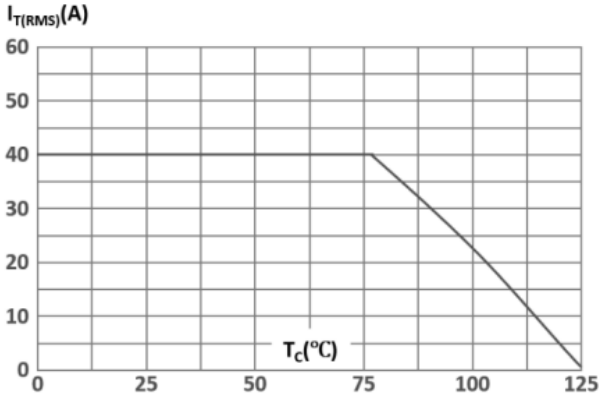


FIG.3: Surge peak on-state current versus number of cycles

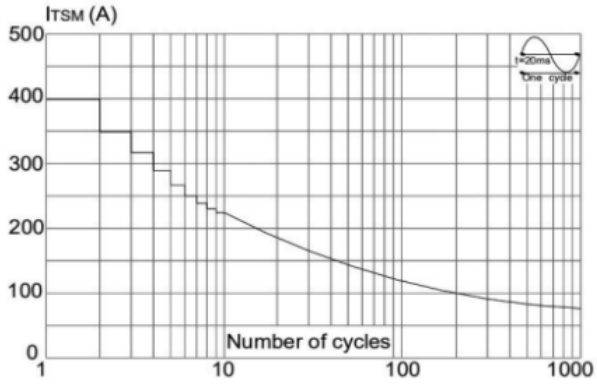


FIG.4:On-state characteristics (maximum values)

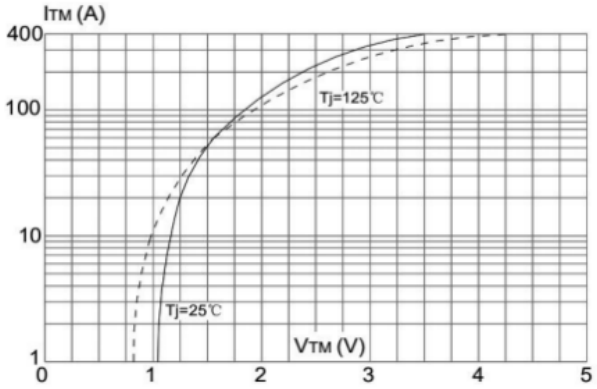


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponding value of I^2t (dI/dt < 50A/μs)

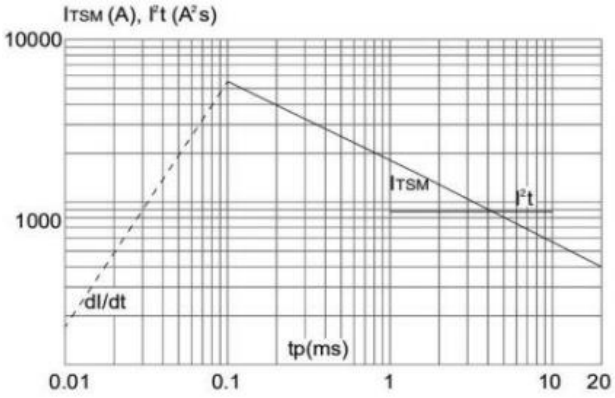
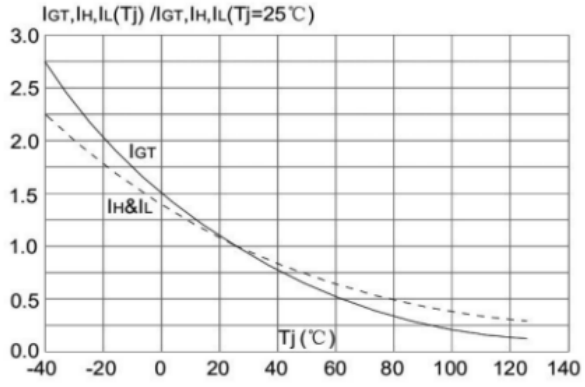
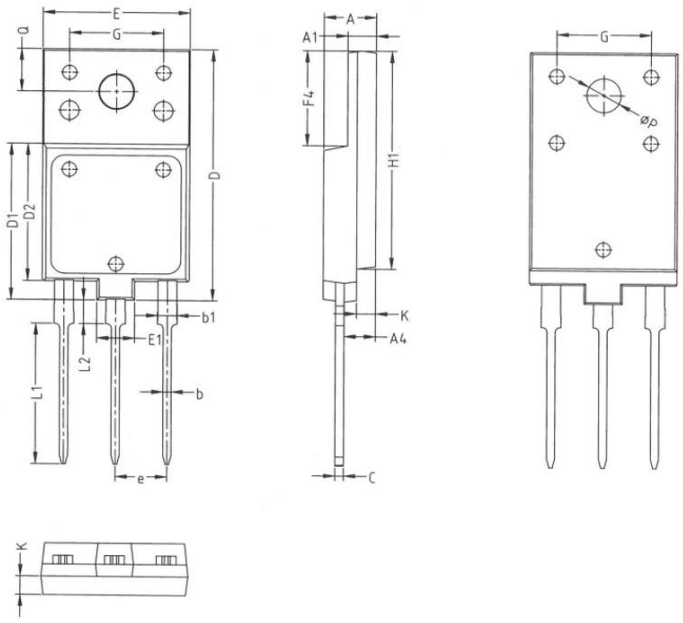


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

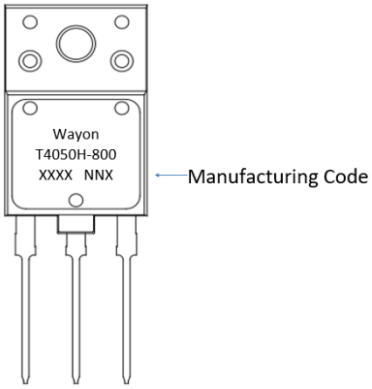


Outline Drawing- TO-3PF

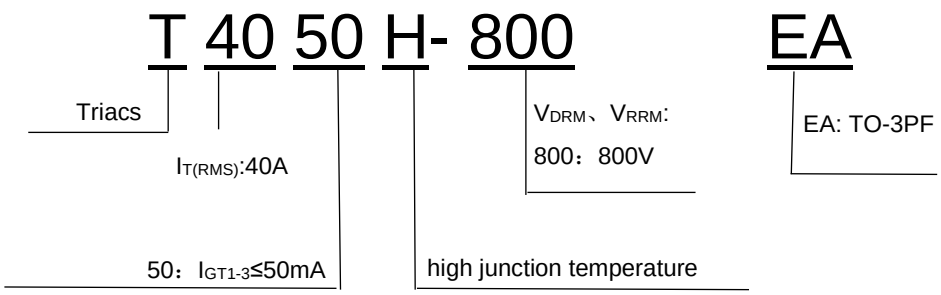
SYMBOL	MM		
	MIN	NOM	MAX
A	5.30	5.50	5.70
A1	2.80	3.00	3.20
b	0.66	0.86	0.95
b1	1.80	2.00	2.20
A4	3.10	3.30	3.50
C	0.80	0.90	1.00
D	26.30	26.50	26.70
D1	16.30	16.50	16.70
D2	14.30	14.50	14.70
P	3.40	3.60	3.80
E	15.30	15.50	15.70
E1	3.80	4.00	4.20
e	5.15	5.45	5.75
G	9.70	9.90	10.10
Q	4.35	4.50	4.65
L1	14.60	14.80	15.00
L2	2.30	2.50	2.70
K	1.80	2.00	2.20
F4	9.80	10.00	10.20
H1	22.80	23.00	23.20



Marking Code:



Part Number System



Package Information

Package	Base qty.	Delivery mode
TO-3PF	30	Tube

Contact Information

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The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.

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