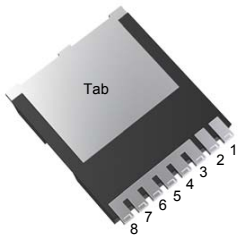
600V 0.051Ω Super Junction Power MOSFET

Description

WMOS™ F2 is Wayon's 2nd generation super junction MOSFET family with fast body diode. F2 series provide all benefits of a fast switching SJ-MOSFET while offering an extremely fast body diode. WMOS™ F2 makes especially resonant switching applications more reliable.

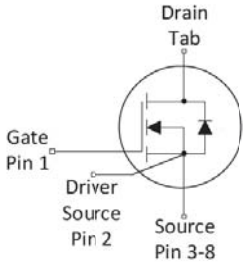


TO-LL

Features

- V_{DS} =650V @ $T_{j,max}$
- yp. $R_{DS(on)}$ =0.051Ω
- 100% UIS tested
- Pb-free plating, Halogen free

RoHS compliant



Applications

LED Lighting, Charger, Adapter, PC, LCD TV, Server

Absolute Maximum Ratings

Parameter	Symbol	WMLL60N60F2	Unit
Drain-source voltage	V_{DSS}	600	V
Continuous drain current[1] (T_C = 25°C)	I_D	60	A
(T_C = 100°C)		36	A
Pulsed drain current[2]	I_{DM}	110	A
Gate-source voltage	V_{GS}	±30	V
Avalanche energy, single pulse[3]	E_{AS}	1050	mJ
Avalanche energy, repetitive[2]	E_{AR}	1.3	mJ
Avalanche current, repetitive[2]	I_{AR}	5	A
Power dissipation (T_C = 25°C)	P_D	400	W
- Derate above 25°C		3.2	W/°C
Operating and storage temperature range	T_j, T_{stg}	-55 to +150	°C
Continuous diode forward current[1]	I_S	60	A
Diode pulse current[2]	$I_{S,pulse}$	110	A

Thermal Characteristics

Parameter	Symbol	WMLL60N60F2	Unit
Thermal resistance, junction-to-case	$R_{\theta JC}$	0.31	°C/W
Thermal resistance, junction-to-ambient	$R_{\theta JA}$	62	°C/W

Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0 V, I_D=1 mA	600	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=0.25mA	3	4	5	V
Drain cut-off current	I_{DSS}	V_{DS}=600 V, V_{GS}=0V,				μA
		T_j = 25°C	-	-	50	
		T_j = 125°C	-	200	-	
Gate leakage current, forward	I_{GSSF}	V_{GS}=20V, V_{DS}=0V	-	-	300	nA
Gate leakage current, reverse	I_{GSSR}	V_{GS}=-20V, V_{DS}=0V	-	-	-300	nA
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=10 V, I_D=15A	-			
		T_j = 25°C	-	0.051	0.06	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	V_{DS}= 100V, V_{GS}= 0V,	-	4325	-	
Output capacitance	C_{oss}	f = 1 MHz	-	128	-	pF
Reverse transfer capacitance	C_{rss}		-	2.1	-	
Turn-on delay time	$t_{d(on)}$	V_{DD} = 300V, I_D = 15A	-	75	-	
Rise time	t_r	R_G = 25Ω, V_{GS}=10V	-	57	-	ns
Turn-off delay time	$t_{d(off)}$		-	240	-	
Fall time	t_f		-	15	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	V_{DD}=480V, I_D=15A,	-	16.0	-	
Gate to drain charge	Q_{gd}	V_{GS}=0 to 10V	-	21.6	-	nC
Gate charge total	Q_g		-	75.3	-	
Gate plateau voltage	$V_{plateau}$		-	5.5	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V_{GS}=0 V, I_F=15A	-	-	1.2	V
Reverse recovery time	t_{rr}	V_R=50V, I_F=15A,	-	155	-	ns
Reverse recovery charge	Q_{rr}	dI_F/dt=100A/μs	-	1.3	-	μC
Peak reverse recovery current	I_{rrm}		-	16	-	A

Notes:

1. Limited by $T_{j\,max}$. Maximum duty cycle D=0.5.

2. Pulse width limited by maximum junction temperature.

3. I_{AS} = 5A, V_{DD} = 50V, R_G = 25Ω, starting T_j = 25°C.

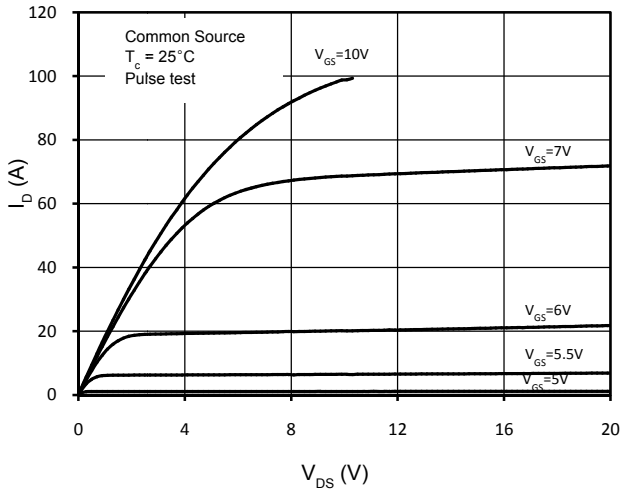


Figure 1.On-Region Characteristics

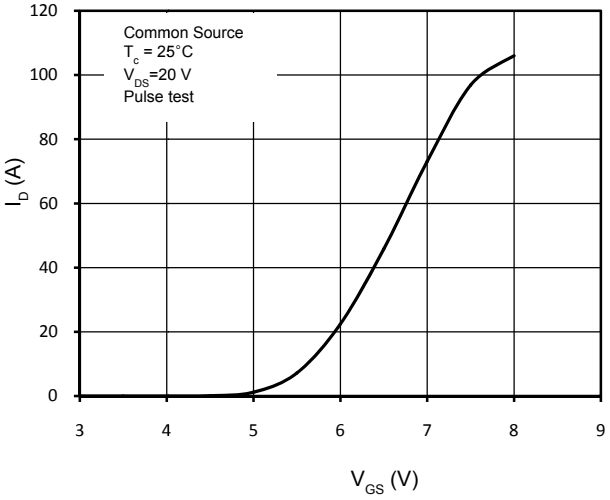


Figure 2. Transfer Characteristics

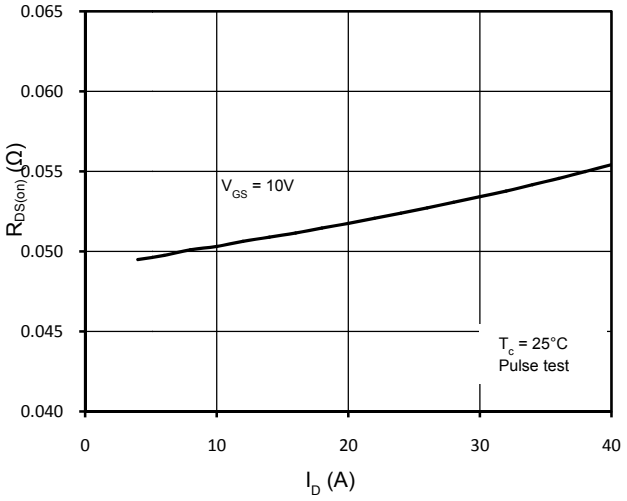


Figure 3. Static Drain-Source On Resistance

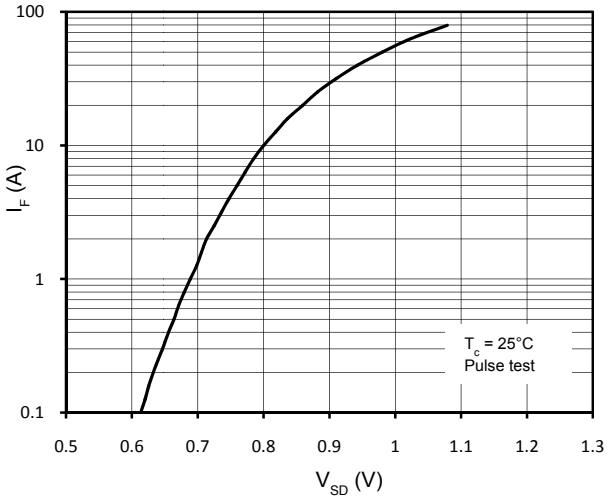


Figure 4. Body- Diode Forward Characteristics

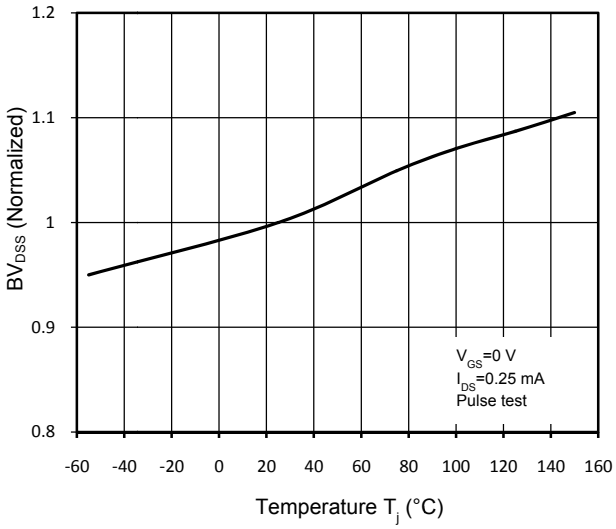


Figure 5. Normalized BV_{DSS} vs. Temperature

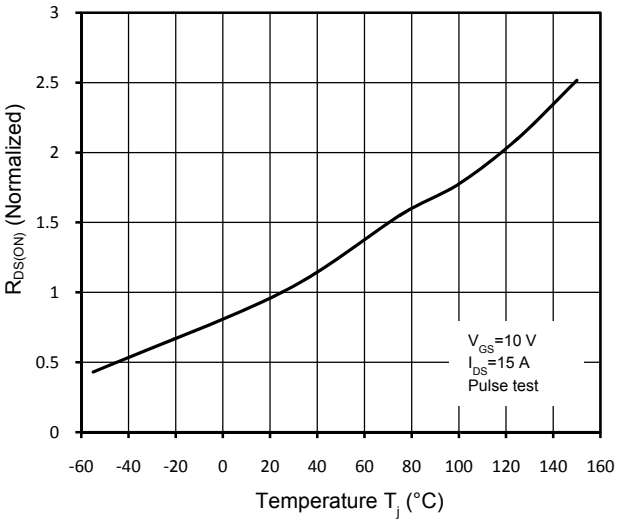


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

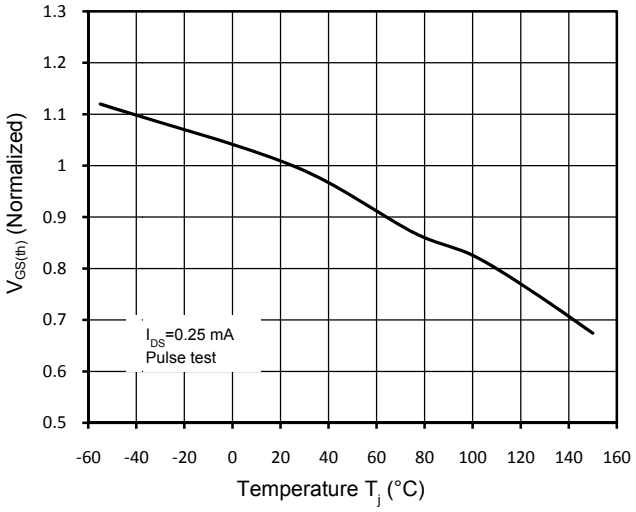


Figure 7. Threshold Voltage vs. Temperature

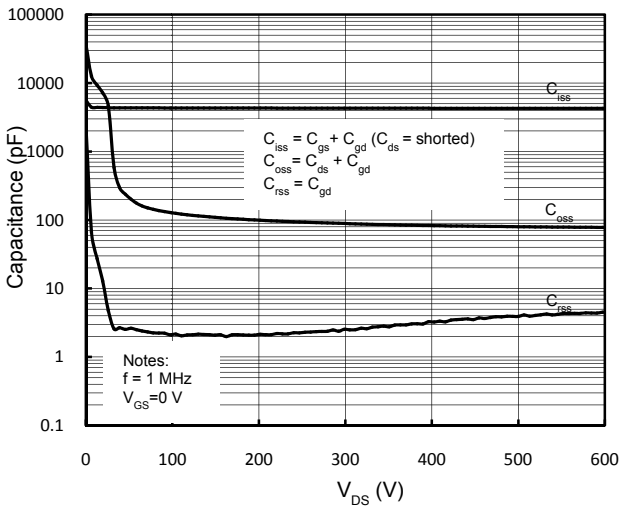


Figure 8. Capacitance Characteristics

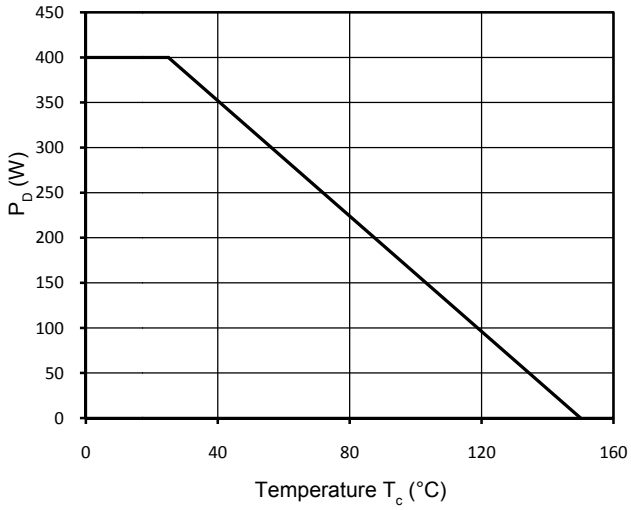


Figure 9. Power Dissipation

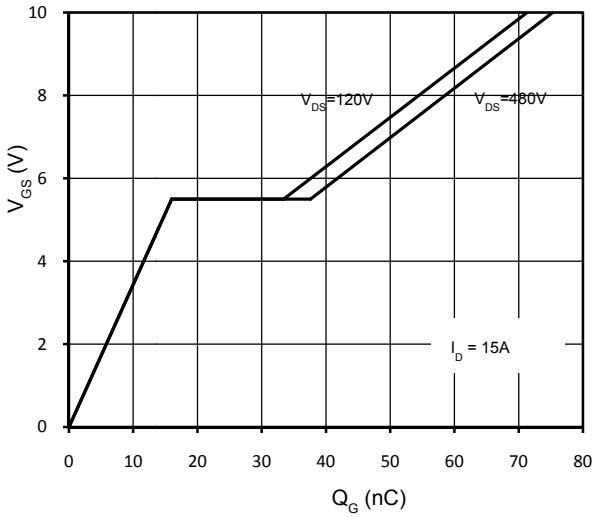


Figure 10. Gate Charge Characteristics

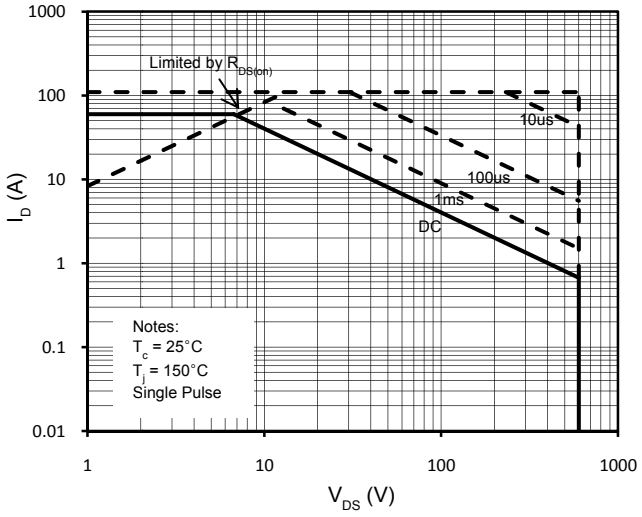


Figure 11. Maximum Safe Operating Area

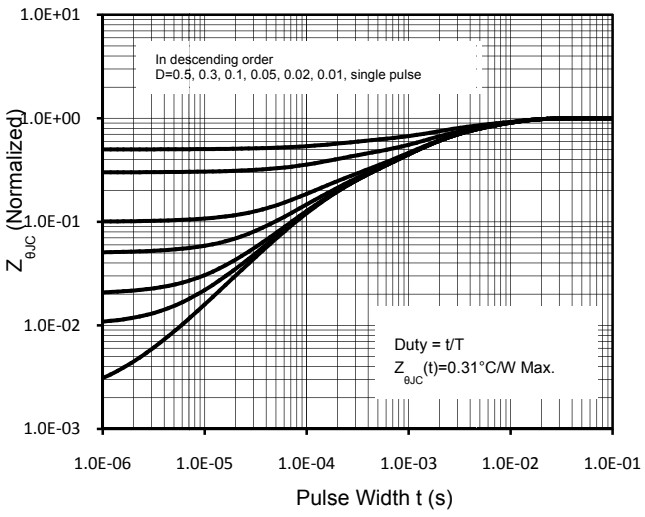
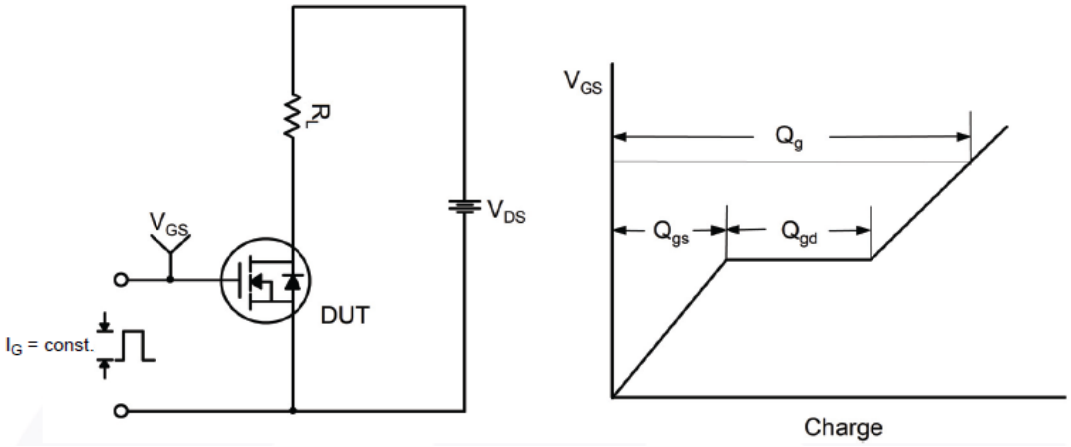
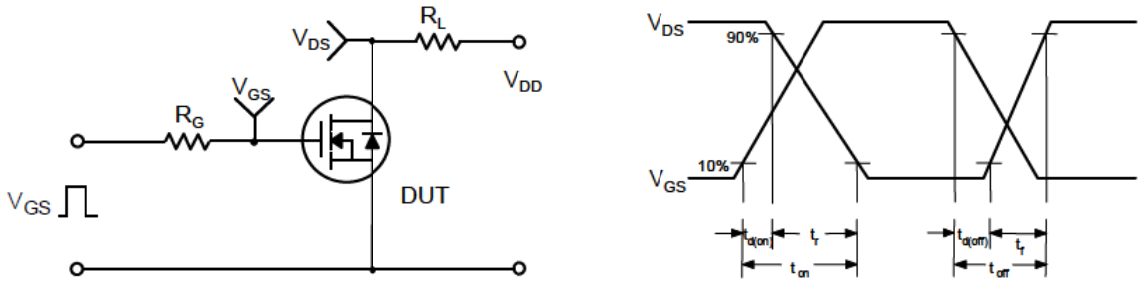


Figure 12. Transient Thermal Response Curve

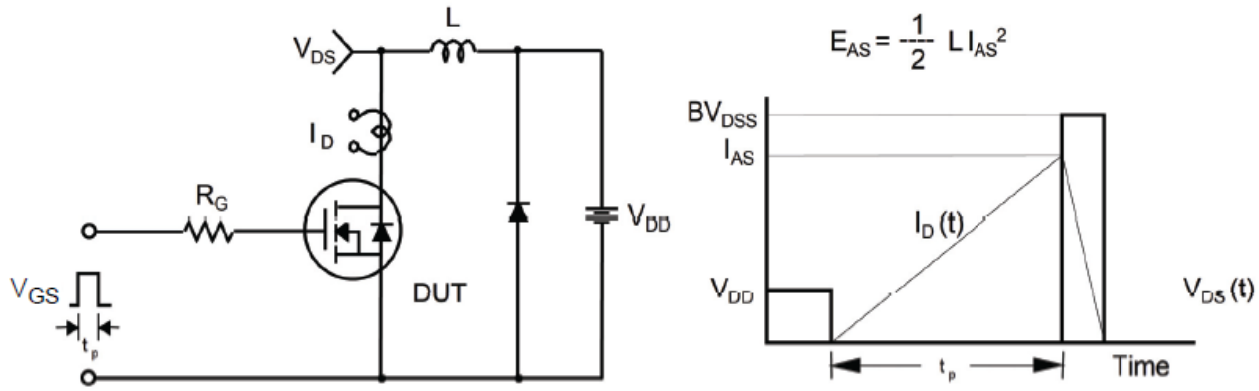
Gate Charge Test Circuit & Waveform



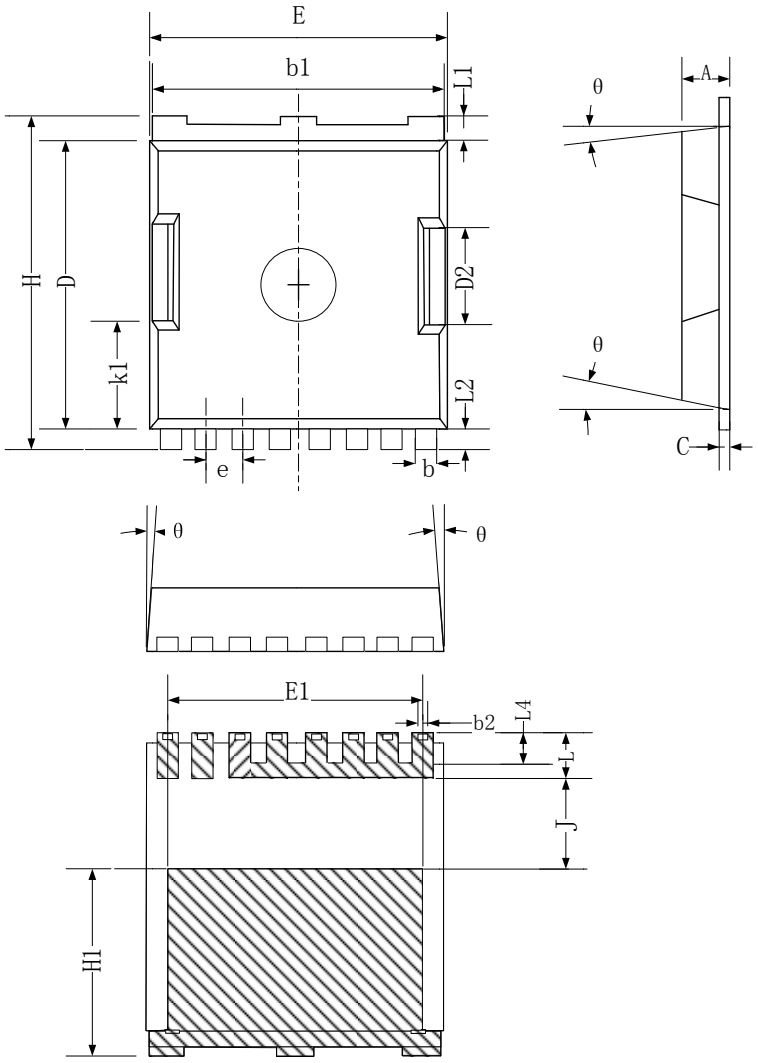
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions for TO-LL



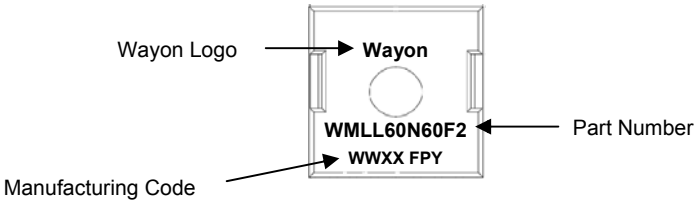
COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.2	2.3	2.4
b	0.7	0.8	0.9
b1	9.6	9.75	9.9
b2	0.38	-	0.5
c	0.4	0.5	0.6
D	10.1	10.4	10.6
D2	3.10	3.30	3.50
E	9.60	9.90	10.10
E1	7.90	8.20	8.50
e	1.20BSC		
H	11.38	11.60	11.88
H1	6.65	6.85	7.15
N	8		
J	2.95	3.15	3.30
K1	3.8	4.1	4.4
L	1.40	1.60	1.80
L1	0.50	0.65	0.80
L2	0.50	0.65	0.80
L4	1.00	1.15	1.30
θ	4°	7°	10°

Ordering Information

Part	Package	Marking	Packing method
WMLL60N60F2	TO-LL	WMLL60N60F2	Tape and Reel

Marking Information



Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201202
Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com
WAYON website: **http://www.way-on.com**
For additional information, please contact your local Sales Representative.

WAYON ® is registered trademarks of Wayon Corporation.

Disclaimer

WAYON reserves the right to make changes without further notice to any Products herein to improve reliability, function, or design. The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. WAYON does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Products or technical information described in this document.