1. General Descriptions

The WD2305 is a high-efficiency synchronous buck converter that can deliver up to 1.2A output current. It integrates the main switch and synchronous switch inside, and has extremely low on-resistance to reduce conduction loss. The switching frequency is 1.5MHz, which can reduce the size of the external inductor and output capacitor and reduce the output voltage ripple.

The WD2305 regulator is available in Green SOT23-5L and SOT23-6L package.

2.Features

- 2.5V~5.5V input voltage range
- High Efficiency: Up to 95%
- 35μA ultra-low quiescent current
- Internal soft-start
- Input Overvoltage Protection (OVP)
- Short circuit protection Hiccup mode
- Internal integrated low $R_{DS(ON)}$ switch
- 1.5MHz switching frequency
- 100% duty cycle
- RoHS Compliant and Halogen Free

3.Applications

- Set-top box, security camera
- Mobile phones, handheld game consoles, media players
- Electric toys, meters, etc.

4.Typical Application

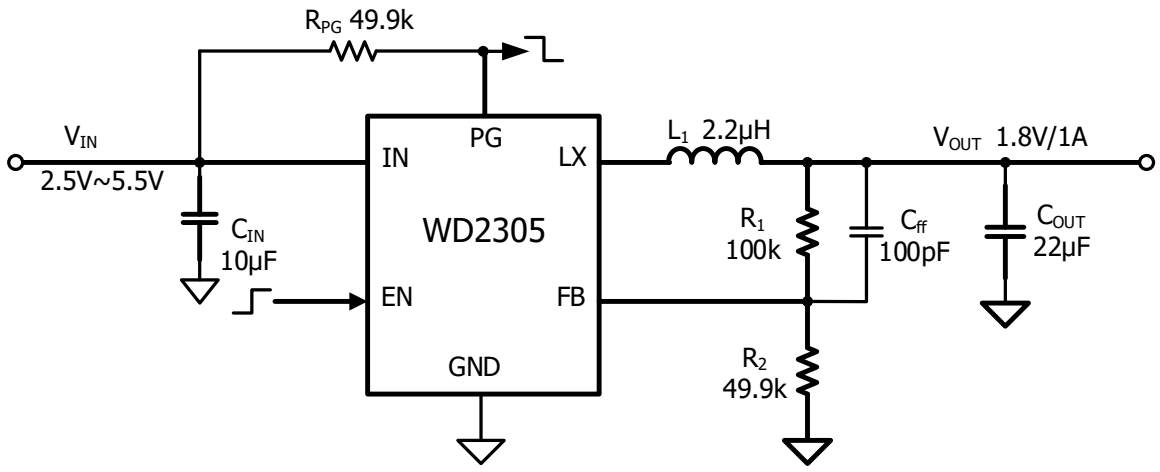
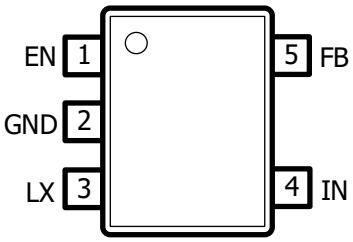


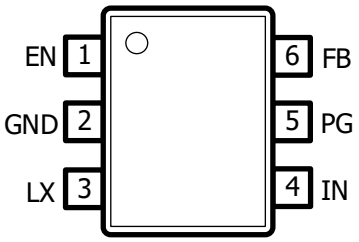
Figure 1. Simplified Schematic

5. Pin Configuration

(Top View)



SOT23-5L



SOT23-6L

6. Pin Description

PIN NUMBER		PIN NAME	I/O	PIN FUNCTIONS
SOT23-5L	SOT23-6L			
1	1	EN	I	Enable signal input pin, the chip works when input high level, do not leave the pin floating.
2	2	GND	-	Ground pin.
3	3	LX	O	Switching node pin. This pin should be connected with the inductor.
4	4	IN	I	Power input pin. Use a ceramic decoupling capacitor of at least 4.7μF to connect this pin to ground.
-	5	PG	O	Power good pin, indicating whether the output power is ready.
5	6	FB	I	Feedback pin. Connect this pin to the center point of the output divider resistor to set the output voltage.

7. Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) [1]

SYMBOL	PARAMETER		RATING	UNIT
V_{IN}	IN Pin Voltage Range to GND		-0.3~6.5	V
V_{LX}	LX Pin Voltage Range to GND		-0.3~6.5	V
V_{EN}, V_{FB}, V_{PG}	EN, FB, PG Voltage		-0.3~6.5	V
T_{STG}	Storage Junction Temperature		-55~150	°C
T_J	Maximum Junction Temperature		150	°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	SOT23-5/6L	150	°C /W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	SOT23-5/6L	70	°C /W
ESD	HBM		2	kV
	CDM		1	kV

NOTE1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

8. Recommended Operating Conditions

SYMBOL	PARAMETER	MIN	TYP	MAX	UNIT
V_{IN}	Input Voltage	2.5		5.5	V
V_{OUT}	Output Voltage	0.6		V_{IN}	V
I_{OUT}	Output Current		1.0	1.2	A
T_J	Operating Junction Temperature	-40		125	°C

9.Electrical Characteristics

(V_{IN} = 5V, L_1 = 2.2μH, C_{IN}=10μF, C_{OUT}=22μF, T_A = 25℃, unless otherwise noted)

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{IN}	Input Voltage Range	-	2.5	5	5.5	V
V_{UVLO}	Input UVLO threshold	V_{IN} rising		2.3	2.45	V
V_{HYS}	UVLO hysteresis			0.18		V
V_{IN_OVP}	V_{IN} OVP Threshold	V_{IN} rising		6.2		V
I_Q	Quiescent Current	I_{OUT}=0, V_{FB}=V_{REF}*105%		35	65	μA
I_{SHDN}	Shutdown Current	V_{EN}=0V		0.1	1.0	μA
V_{REF}	Feedback Reference Voltage		0.588	0.600	0.612	V
$R_{DS(ON)_P}$	PFET RON			200		mΩ
$R_{DS(ON)_N}$	NFET RON			100		mΩ
I_{LIM}	PFET Current Limit	V_{OUT}=2.5V	1.8			A
R_{DIS}	LX Discharge Resistance	V_{EN}=0V		50		Ω
V_{ENH}	EN rising threshold	-	1.5			V
V_{ENL}	EN falling threshold	-			0.4	V
I_{EN_LK}	EN Leakage Current			0.1	2.0	μA
V_{PG_TH}	Power Good Threshold	PG low, FB falling		90		%
		PG high, FB rising		90		%
I_{PG_SINK}	Power Good Sink Current				2	mA
F_{OSC}	Oscillator Frequency	I_{OUT}=0.5A		1.5		MHz
		V_{OUT}=0V		400		kHz
t_{ON_MIN}	Min ON Time			60		ns
D_{MAX}	Max Duty Cycle		100			%
T_{SS}	Soft Start Time			500		μs
T_{SD}	Thermal Shutdown Temperature			175		°C
T_{HYS}	Thermal Shutdown Hysteresis			35		°C

10.Typical Performance Characteristics

(V_{IN} = V_{EN} = 5V, V_{OUT} = 1.8V, C_{IN} =10uF, C_{OUT} = 22μF, L_1 = 2.2μH, T_A = 25°C, unless otherwise noted)

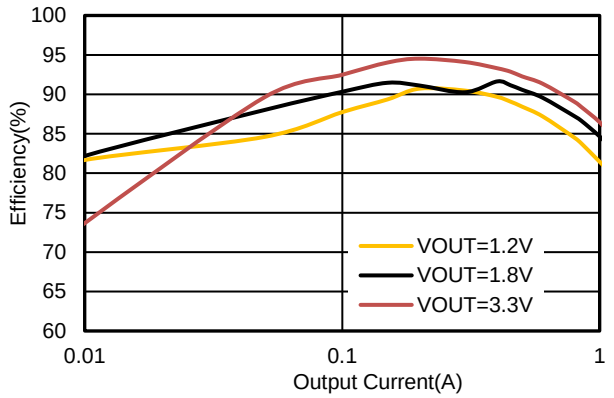


Figure 2. Efficiency vs. Output Current

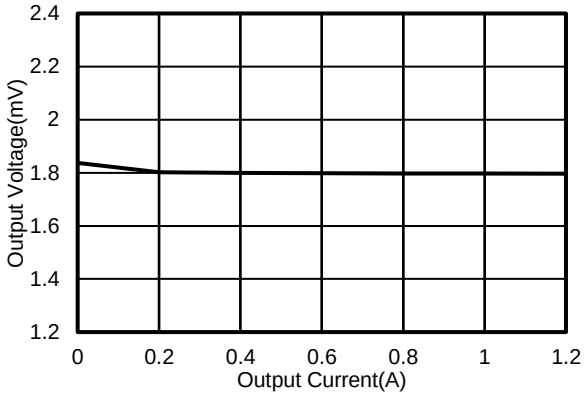


Figure 3. Output Voltage vs. Output Current

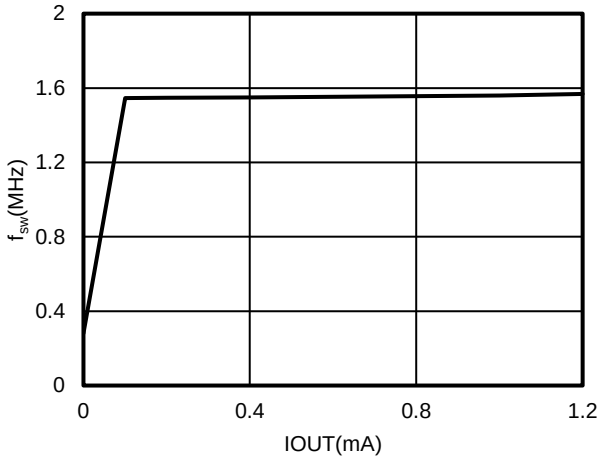


Figure 4. Oscillator Frequency vs. Output Current

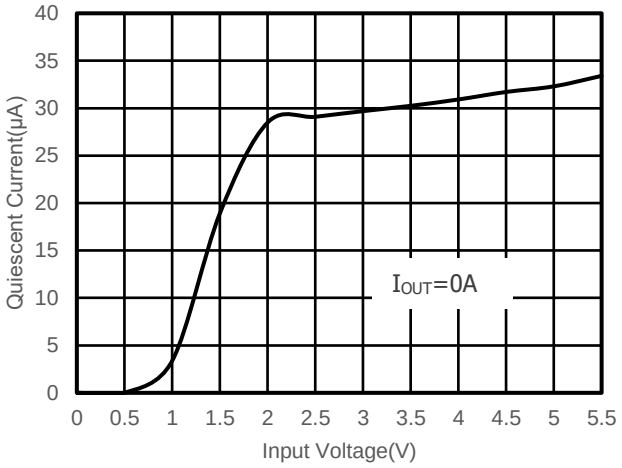


Figure 5. Quiescent Current vs. Input Voltage

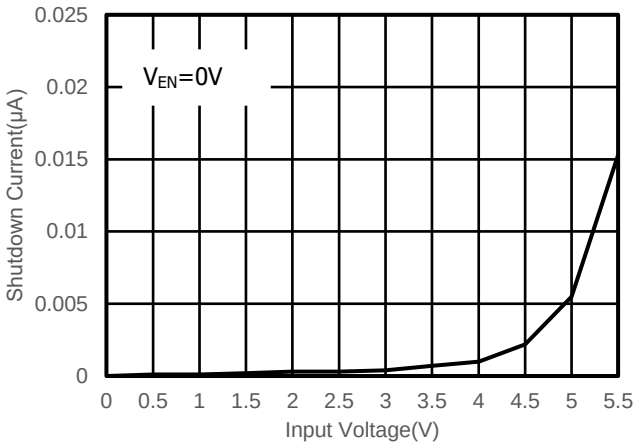


Figure 6. Shutdown Current vs. Input Voltage

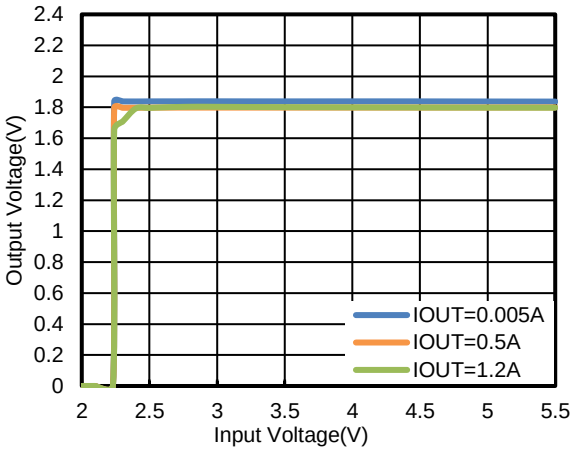


Figure 7. Output Voltage vs. Input Voltage

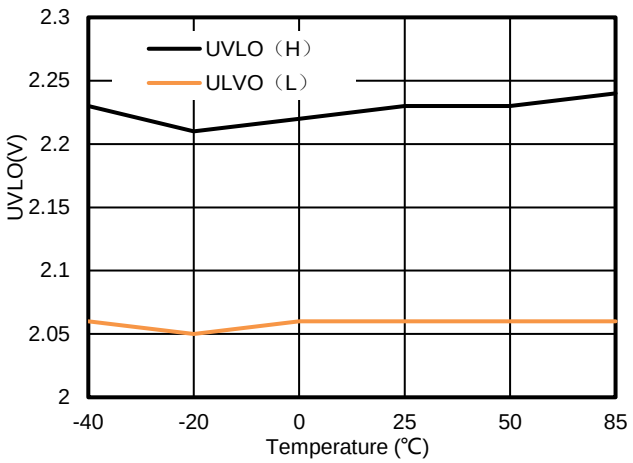


Figure 8. UVLO vs. Temperature

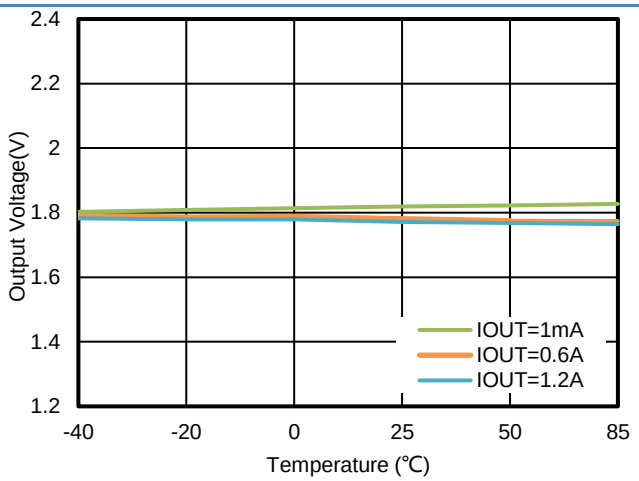


Figure 9. Output Voltage vs. Temperature

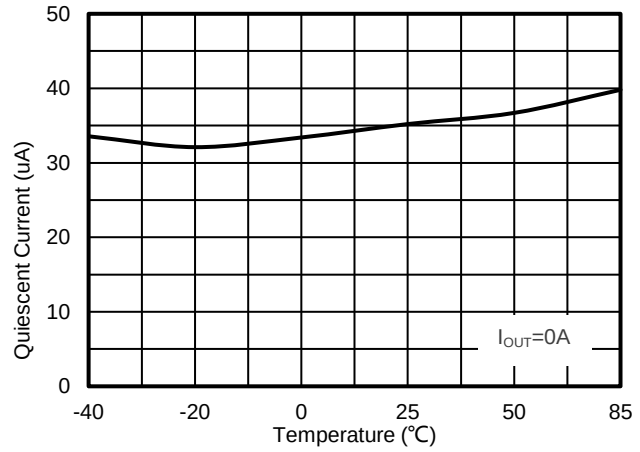


Figure 10. Quiescent Current vs. Temperature

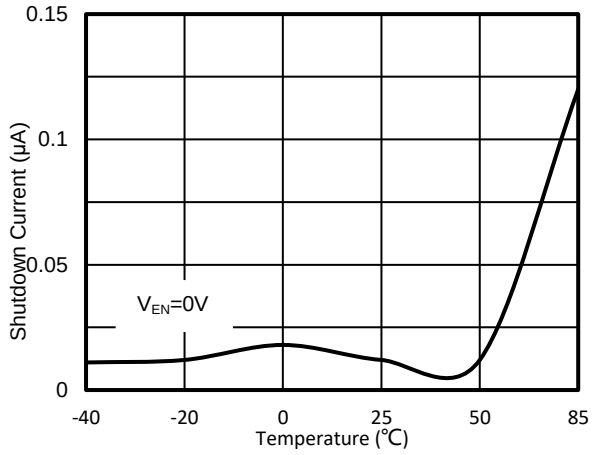


Figure 11. Shutdown Current vs. Temperature

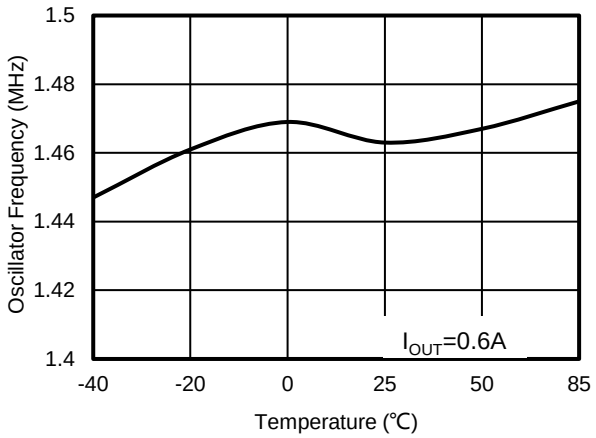


Figure 12. Oscillator Frequency vs. Temperature

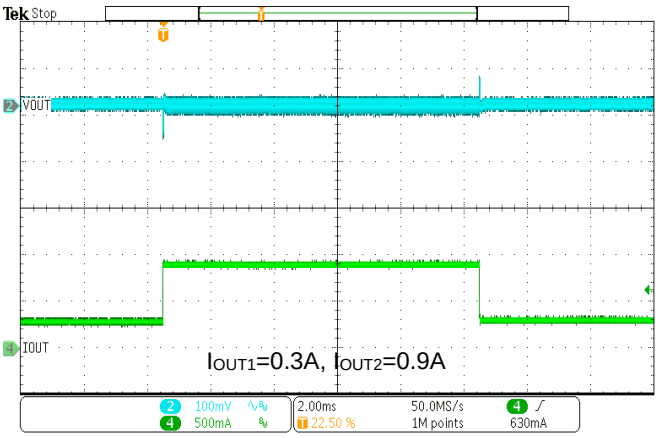


Figure 13. Load Transient

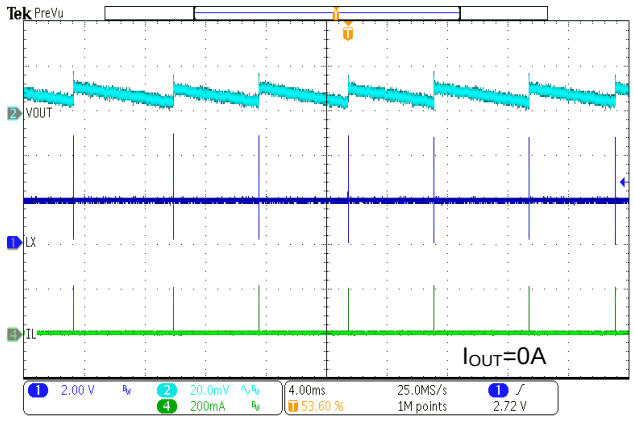


Figure 14. Output Ripple

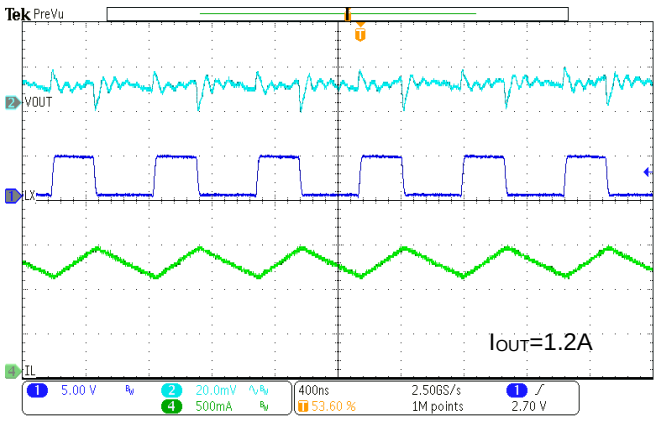


Figure 15. Output Ripple

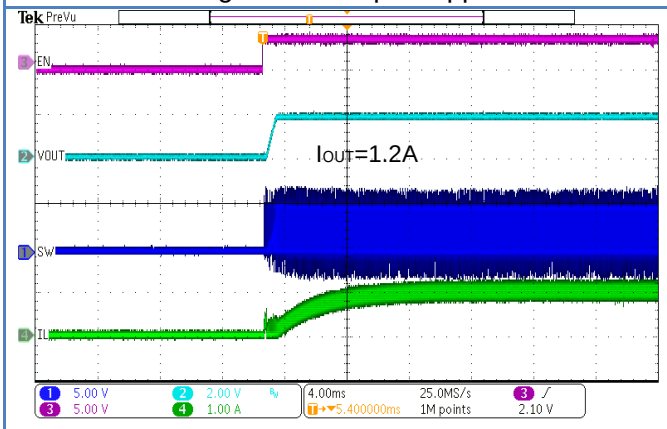


Figure 16. Startup from Enable

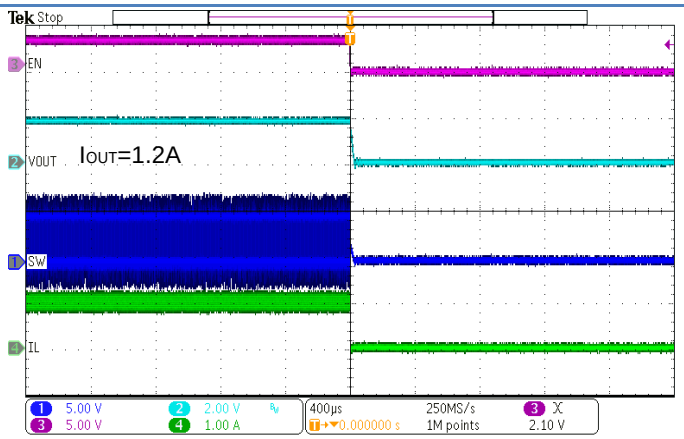


Figure 17. Shutdown from Enable

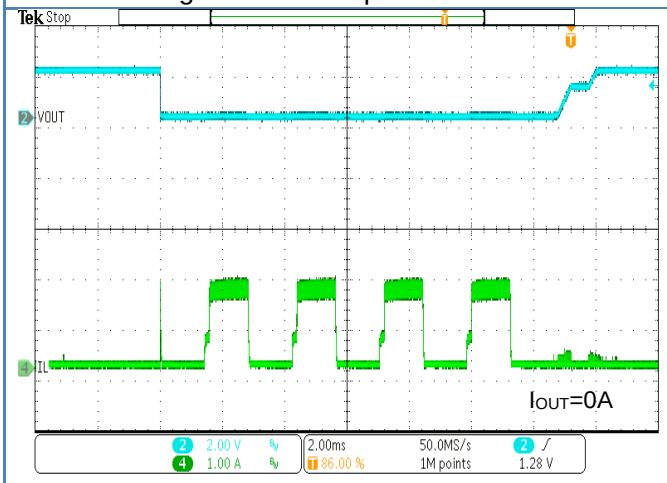


Figure 18. Short Circuit Protection and Recovery

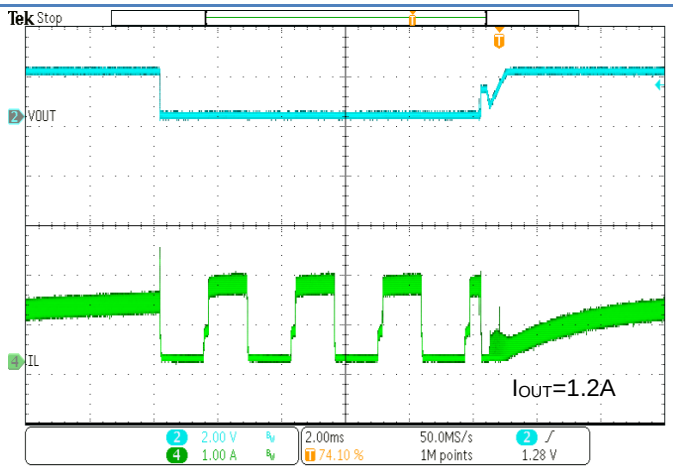
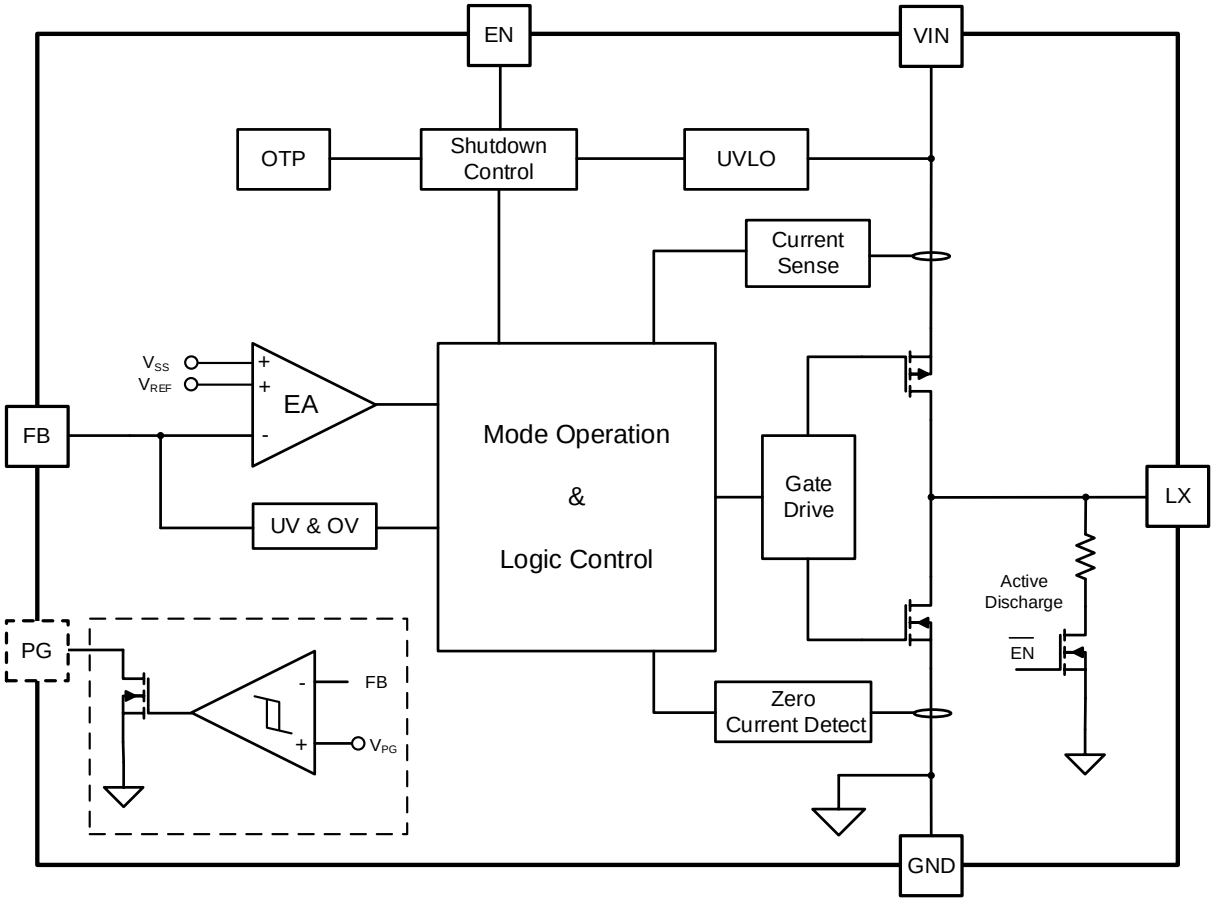


Figure 19. Short Circuit Protection and Recovery

11.Function Description

11.1 Block Diagram



Note: Power Good feature is only available in WD2305-A60R

11.2 V_{IN}-UVLO

The chip has an under-voltage lockout function, which ensures that the chip will not start until the input voltage reach the specified voltage, and a hysteresis function is set for UVLO to ensure that noise on the power supply does not cause system failure.

11.3 EN

When the input voltage is greater than the under-voltage lockout (UVLO) threshold (typically 2.3V), the chip can be enabled by pulling EN higher than 1.5V. Pulling it down to ground disables the chip. When the chip is disabled, it goes into output discharge mode automatically. Its internal discharge MOSFET provides a resistive discharge path for the output capacitor. EN can be directly connected with V_{IN} for default enable-on, and floating EN is not permitted.

11.4 Soft Start

During the power-up of the chip, the output voltage rises from zero to the regulated level. Due to the effect of negative feedback, the duty cycle of the PWM control signal of the power MOSFET changes from the maximum value and gradually decreases until the circuit is stable. Due to the existence of output filter capacitors in the circuit, it is easy to generate inrush current when charging the capacitors. At this time, a large current will flow through the power MOSFET, which is easy to cause the system fail. Therefore, there is a soft start function built in the chip, and the output voltage gradually increases from 0V to the regulated level during power-on, reducing the input inrush current.

11.5 PWM/PFM Mode

The PWM/PFM hybrid control method is to stabilize the output voltage by changing the width and pulse frequency of the square wave. At full load, PWM is used, which has relatively high efficiency and a wide range of duty cycles. The PFM mode is used to improve the efficiency at light load.

11.6 Feedback

The FB will be connected to the internal EA of the chip and compared with the internal reference voltage(0.6V). The result of the comparison will control the change of the duty cycle, so as to achieve the purpose of stabilizing the voltage.

11.7 Output Protection

The chip has a minimum 1.8A high-side switch peak current limit. When the high-side switch reaches its peak current limit, it remains in hiccup mode until the current drops. This prevents the inductor current from continuing to rise and damaging components.

The chip enters short-circuit protection mode when it reaches the peak current limit under short-circuit condition and attempts to recover with hiccup mode. In this process, the chip disables the output power stage, discharges the soft-start capacitor, and then attempts to soft start again automatically. If the short-circuit condition remains after the soft start ends, the chip repeats this cycle until the short-circuit disappears and the output rises back to the regulation level.

11.8 Power Good (only for WD2305-A60R)

The chip has an open drain PG and requires an external pull-up resistor between 100kΩ~500kΩ for the power good. Note: Keep PG pull-up voltage is not more than V_{IN}. When V_{OUT} is within 90% -100% of the regulated level, V_{PG} is pulled up to high by the external resistor. If V_{OUT} exceeds the 90% -100% window, the internal MOSFET pulls PG to ground.

12. Application Information

12.1 Typical Application Circuit

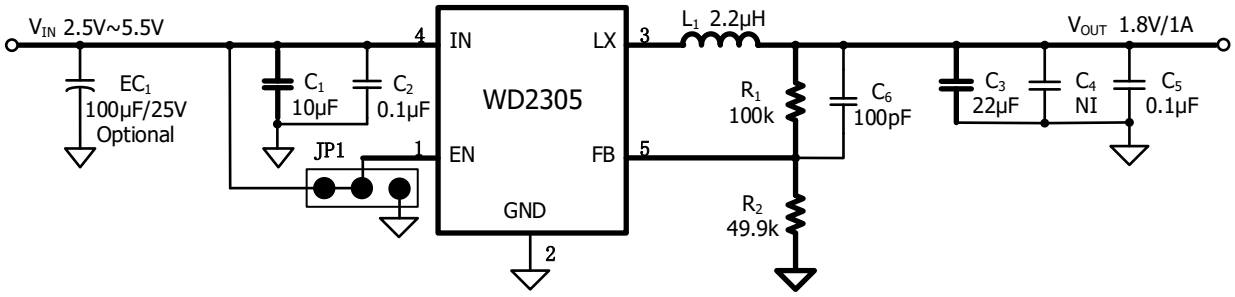


Figure 20. WD2305 Typical Application Circuit

12.2 Setting the Output Voltage

The output voltage is set by an external resistor divider according to Equation:

$$V_{OUT} = V_{FB} \times \left(1 + \frac{R_1}{R_2}\right) = 0.6V \times \left(1 + \frac{R_1}{R_2}\right)$$

There is no strict requirement for the feedback resistor. An R_1 value greater than 10kΩ is reasonable for most applications. R_2 must not be higher than 100 kΩ to achieve high efficiency at light load while providing acceptable noise sensitivity.

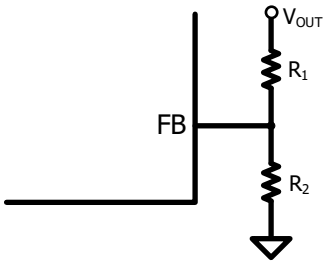


Figure 21. Feedback Network

Table1 shows a list of recommended component selections for common WD2305 output voltages.

Table 1. Recommended Components

Output Voltage (V)	R_1 (kΩ)	R_2 (kΩ)	L_1 (μH)	C_1 (μF)	C_3 (μF)	C_6 (pF)
1.0	40.2	60.4	1.0	10	22	100
1.2	49.9	49.9	1.0	10	22	100
1.5	60.4	40.2	1.5	10	22	100
1.8	100	49.9	2.2	10	22	100
2.5	76.8	24	2.2	10	22	100
3.3	90.9	20	2.2	10	22	100

12.2 Selecting the Inductor

Most applications work best with a 1 - 2.2μH inductor. Select an inductor with a DC resistance less than 50mΩ to optimize efficiency. A high-frequency, switch-mode power supply with a magnetic device produces a strong electronic magnetic inference in the system. Any shield inductor, are ideal for applications as they can decrease the influence effectively.

For most designs, estimate the inductance value with Equation:

$$\mathrm{L} = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_s}$$

Where ΔI_L is the inductor ripple current.

Choose an inductor current that is approximately 30% of the maximum load current. The maximum inductor peak current can be calculated with Equation:

$$I_{PEAK} = I_{LOAD} + \frac{\Delta I_L}{2}$$

12.3 Selecting the Input Capacitor

A typical X5R or better grade ceramic capacitor with 10V rating and no less than 4.7μF capacitance is recommended. To minimize the potential noise problem, we place this ceramic capacitor really close to the V_{IN} and GND pins. Care should be taken to minimize the loop area formed by C_1, and IN/GND pins. An optional E-cap 100uF/25V EC1 should be used for hot-swap application when powered by external power supply with over 1000mm wires.

When using ceramic capacitors, ensure that they have enough capacitance to provide a sufficient charge to prevent an excessive voltage ripple at the input. The input voltage ripple caused by the capacitance can be estimated with equation:

$$\Delta V_{IN} = \frac{I_{LOAD}}{f_s \times C_1} \times \frac{V_{OUT}}{V_{IN}} \times \left(1 - \frac{V_{OUT}}{V_{IN}}\right)$$

12.4 Selecting the Output Capacitor

The output capacitor is selected to handle the output ripple noise requirements. Both steady state ripple and transient requirements must be taken into consideration when selecting this capacitor. Estimate the output voltage ripple with Equation:

$$\Delta V_{OUT} = \frac{V_{OUT}}{f_s \times L} \times (1 - \frac{V_{OUT}}{V_{IN}}) \times (R_{ESR} + \frac{1}{8 \times f_s \times C_2})$$

Where L is the inductor value, and RESR is the equivalent series resistance (ESR) value of the output capacitor. For the best performance, it is recommended to use X5R or better grade ceramic capacitor with 10V rating and no less than 10µF capacitance.

12.5 PCB Layout Guidelines

For 1.5MHz high-frequency switching converter, a poor layout design can result in poor line or load regulation and stability issues. For best performance, refer to Figure 22 and follow the guidelines below.

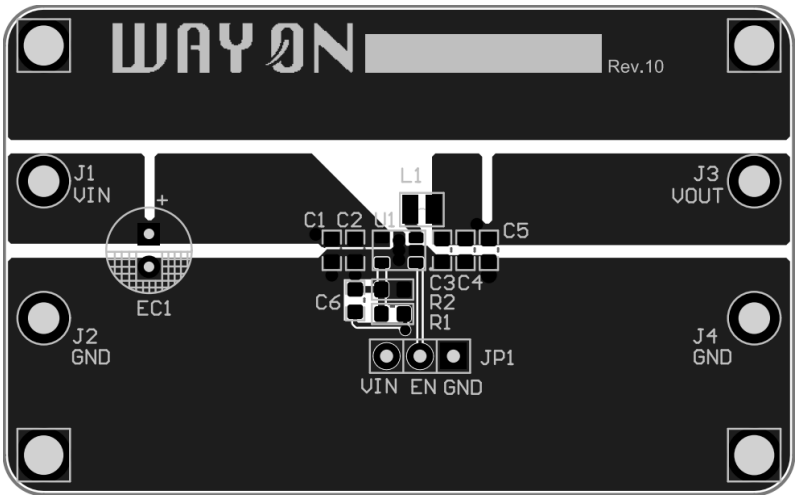


Figure 22. WD2305 PCB Layout

1. Place the high-current paths (GND, IN, and LX) very close to the device with short, direct, and wide traces.

2. Place the input capacitors C_1 and C_2 as close to IN and GND as possible.

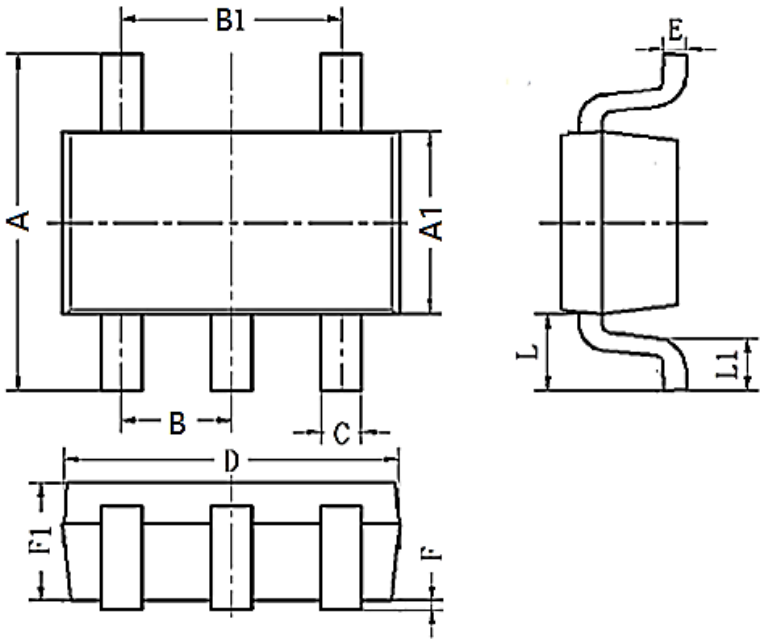
3. Place the external feedback resistors R_1 and R_2 next to FB.

4. Keep the switching node LX short and away from the feedback network.

5. Keep the V_{OUT} sense line as short as possible or away from the power inductor, especially the surrounding inductor.

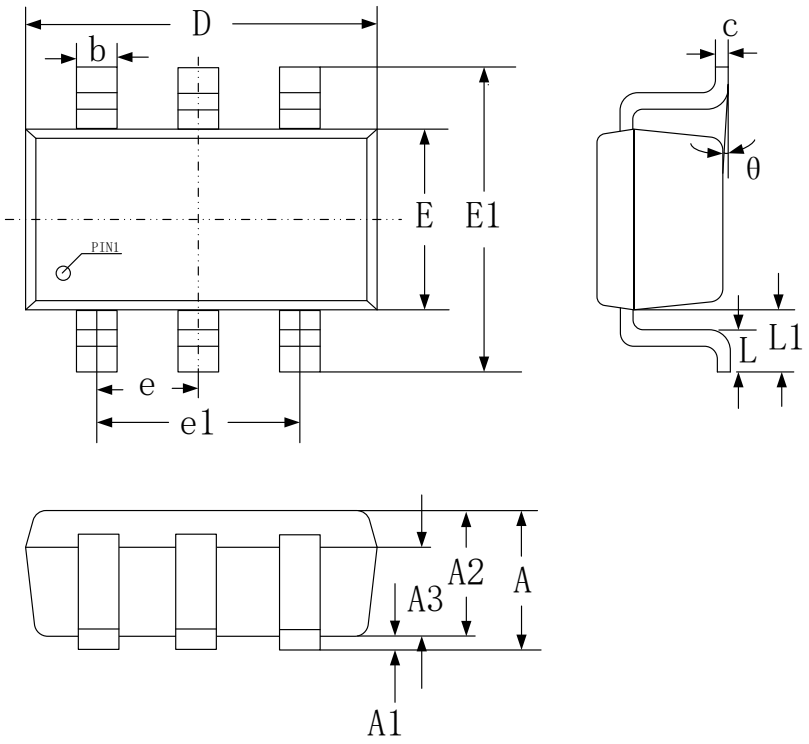
13.Package Information

SOT23-5L



SYMBOL	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX
A	2.60	2.80	3.00
A1	1.50	1.60	1.70
B	0.85	0.95	1.05
B1	1.80	1.90	2.00
C	0.25	0.37	0.50
D	2.79	2.90	3.02
E	0.10	0.15	0.20
F	0.00	0.10	0.20
L	0.60REF		
L1	0.30	0.45	0.60
F1	0.85	1.10	1.30

SOT23-6L



SYMBOL	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX
A	-	-	1.25
A1	0.00	-	0.15
A2	1.00	1.10	1.20
A3	0.575	0.65	0.725
b	0.325	-	0.5
c	0.1	-	0.2
D	2.82	2.92	3.02
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
e	0.925	0.95	0.975
e1	1.90 BSC		
L1	0.59 REF		
L	0.35	0.45	0.60
θ	0°		8°

14.Ordering Information

PART NUMBER	POWER GOOD	PACKAGE	PACKING QUANTITY	MARKING
WD2305-A50R	N	SOT23-5L	3k/Reel	P16XX
WD2305-A60R	Y	SOT23-6L	3k/Reel	P16PXX

* XX is variable.

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For additional information, please contact your local Sales Representative.
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Specifications are subject to change without notice.
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.

Product Specification Statement

- The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.
- The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
- WAYON strives to provide accurate and up-to-date information to the best of our ability. However, due to technical, human, or other reasons, WAYON cannot guarantee that the information provided in the product specification is entirely accurate and error-free. WAYON shall not be held responsible for any losses or damages resulting from the use or reliance on any information in these product specifications. WAYON reserves the right to revise or update the product specification and the products at any time without prior notice, and the user's continued use of the product specification is considered an acceptance of these revisions and updates. Prior to purchasing and using the product, users should verify the above information with WAYON to ensure that the product specification is the most current, effective, and complete. If users are particularly concerned about product parameters, please consult WAYON in detail or request relevant product test reports. Any data not explicitly mentioned in the product specification shall be subject to separate agreement.
- Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.
- The design of the product is intended to meet civilian needs and is not guaranteed for use in harsh environments or precision equipment. It is not recommended for use in systems or equipment such as medical devices, aircraft, nuclear power, and similar systems, where failures in these systems or equipment could reasonably be expected to result in personal injury. WAYON shall assume no responsibility for any consequences resulting from such usage.
- Users should also comply with relevant laws, regulations, policies, and standards when using the product specification. Users are responsible for the risks and liabilities arising from the use of the product specification and must ensure that it is not used for illegal purposes. Additionally, users should respect the intellectual property rights related to the product specification and refrain from infringing upon any third-party legal rights. WAYON shall assume no responsibility for any disputes or controversies arising from the above-mentioned issues in any form.