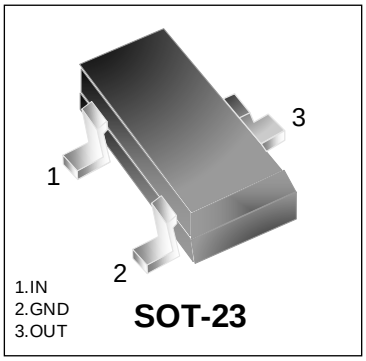
WTDTC143T

NPN Digital Transistors

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



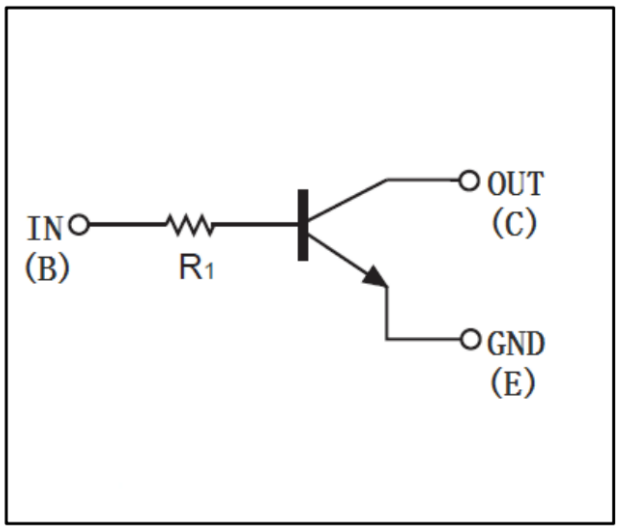
Mechanical Characteristics

- SOT-23 package
- Marking: Marking Code
- Packaging: Tape and Reel
- RoHS Compliant & HF
- Device meets MSL3 requirement

Applications

- Digital application
- Controlling IC inputs
- Switching loads

Equivalent Circuit



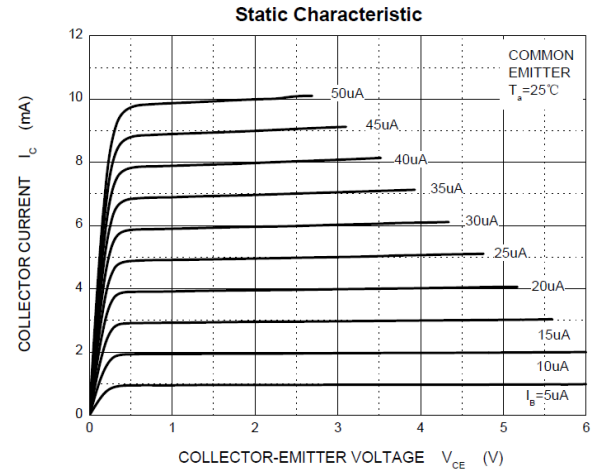
Absolute Maximum Rating(Ta=25°C unless otherwise noted)

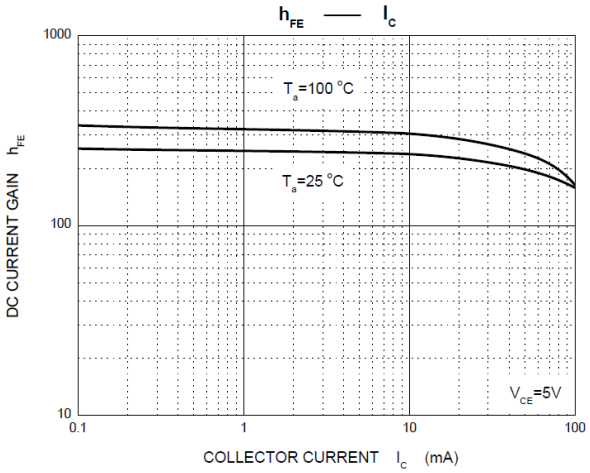
Parameter	Symbol	Value	Unit
Collector Base Voltage	V_{CBO}	50	V
Collector Emitter Voltage	V_{CEO}	50	V
Emitter Base Voltage	V_{EBO}	5	V
Collector Current	I_C	100	mA
Power Dissipation	P_D	200	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55 ~ 150	°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	625	°C/W

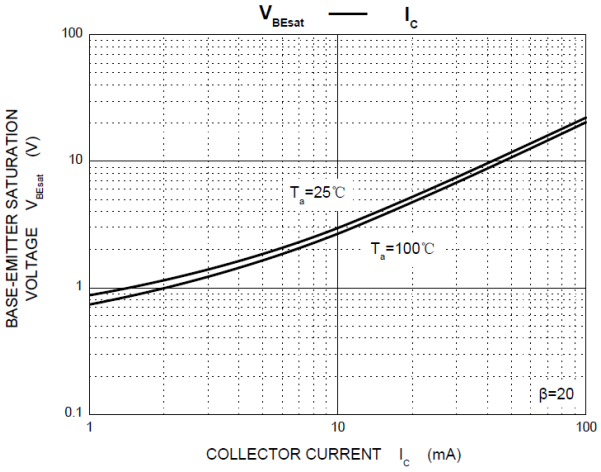
Electrical Characteristics (T_{amb}=25°C unless otherwise specified)

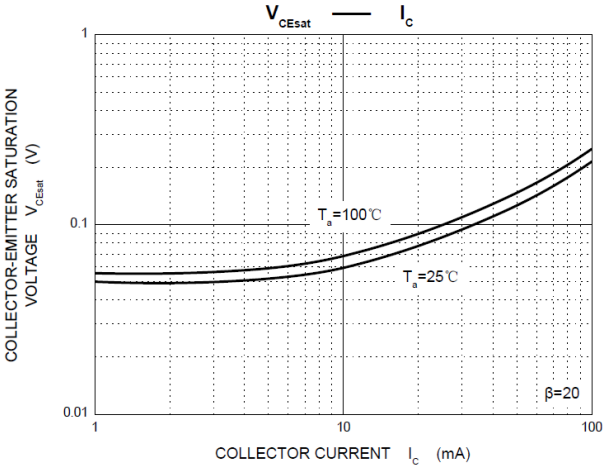
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	I_C = 50μA, I_E = 0	50	-	-	V
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	I_C = 1mA, I_B = 0	50	-	-	V
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	I_E = 50μA, I_C = 0	5	-	-	V
Collector Cut-off Current	I_{CBO}	V_{CB} = 50V, I_E = 0	-	-	500	nA
Emitter Cut-off Current	I_{EBO}	V_{EB} = 4V, I_C = 0	-	-	500	nA
DC Current Gain	h_{FE}	V_{CE} = 5V, I_C = 1mA	100	-	600	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 5mA, I_B = 0.25mA	-	-	0.3	V
Input Resistor	R_1		3.29	4.7	6.11	kΩ
Transition Frequency	f_T	V_{CE}= 10V, I_C= 5mA, f= 100MHz	-	250	-	MHz

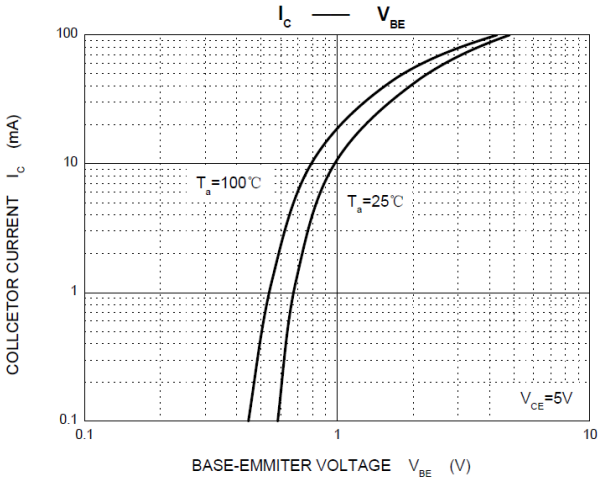
Typical Characteristics

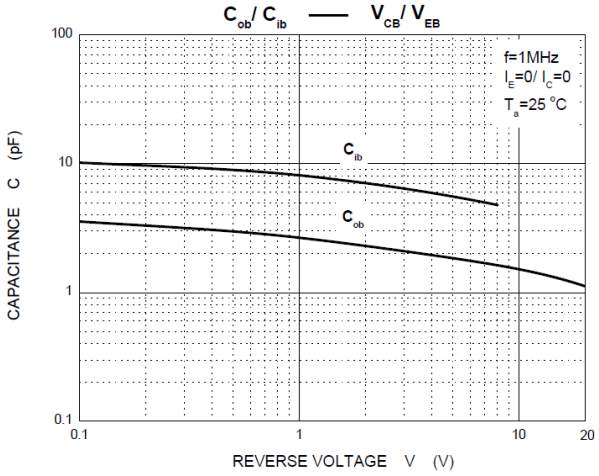






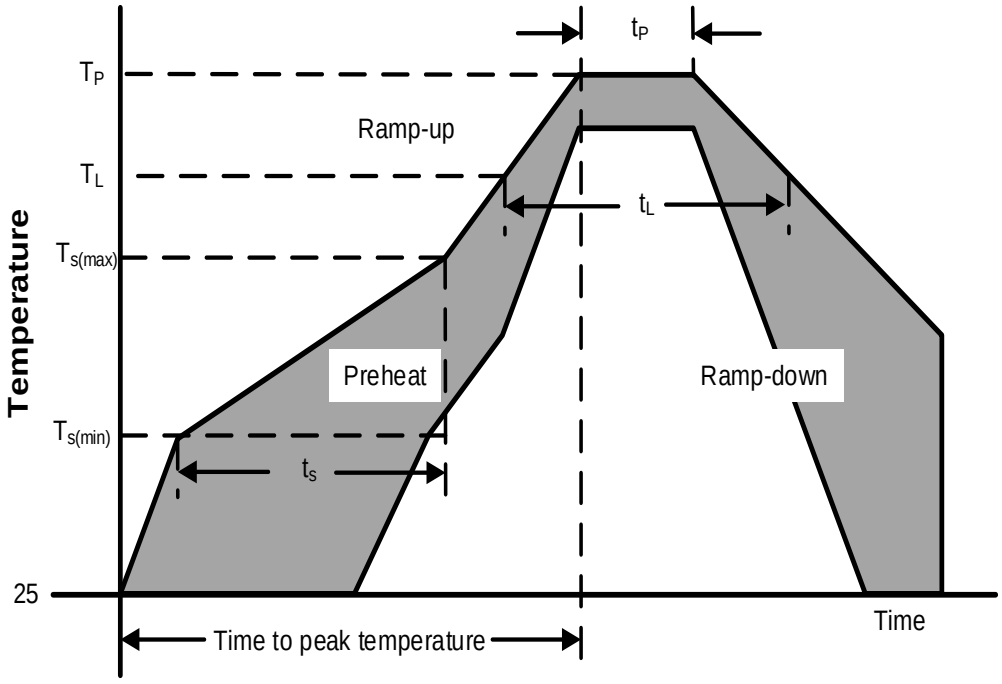






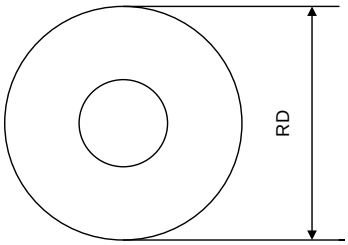
Soldering Parameters

Reflow Condition		Pb – Free assembly
Pre Heat	Temperature Min ($T_{s(min)}$)	150°C
	Temperature Max ($T_{s(max)}$)	200°C
	Time (min to max) (ts)	60 – 190 secs
Average ramp up rate (Liquidus Temp) (T_L) to peak		5°C/second max
$T_{S(max)}$ to T_L——Ramp-up Rate		5°C/second max
Reflow	Temperature (T_L) (Liquidus)	217°C
	Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_P)		260+0/-5 °C
Time within actual peak Temperature (t_p)		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature (T_P)		8 minutes Max.
Do not exceed		280°C

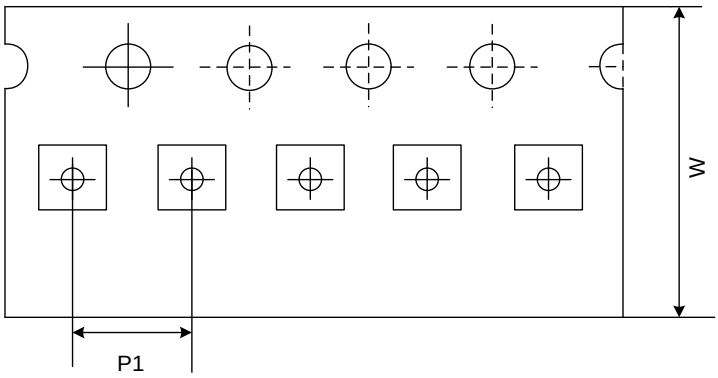


Tape And Reel Information

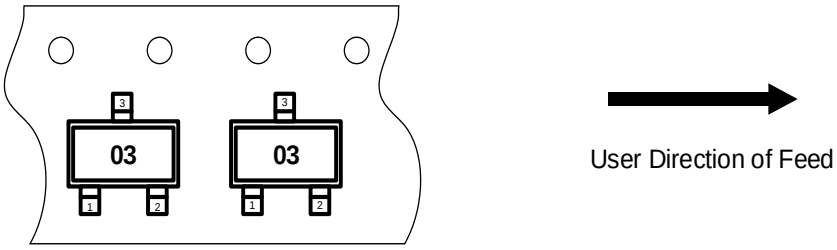
Reel Dimensions



Tape Dimensions



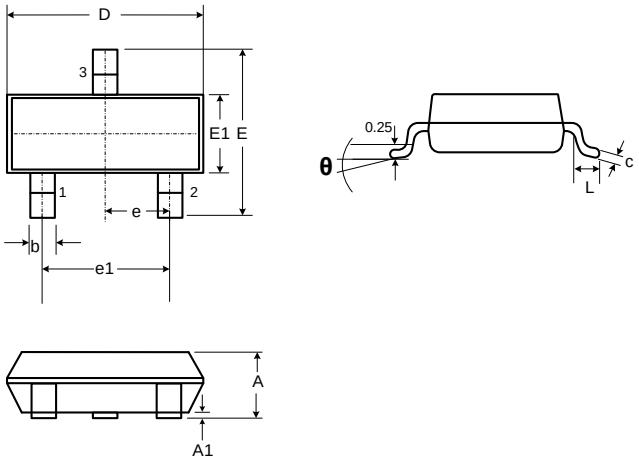
Quadrant Assignments For PIN1 Orientation In Tape

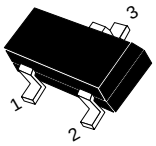


RD	Reel Dimensions	7 inch
W	Overall width of the carrier tape	8 mm
P1	Pitch between successive cavity centers	4mm

Outline Drawing –SOT-23

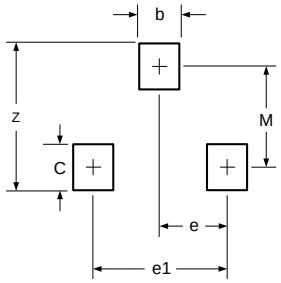
PACKAGE OUTLINE





SOT-23

SYMBOL	MILLIMETERS	
	MIN	MAX
A	0.90	1.15
A1	0.00	0.15
b	0.30	0.55
c	0.07	0.20
D	2.80	3.10
E	2.10	2.65
E1	1.20	1.40
e	0.95 BSC	
e1	1.78	2.05
L	0.30	0.50
θ	0°	8°



DIMENSIONS		
DIM	INCHES	MILLIMETERS
M	0.0795	2.02
C	0.0315	0.80
Z	0.111	2.82
e	0.037 BSC	0.95 BSC
e1	0.075 BSC	1.9 BSC
b	0.0315	0.80

Notes:
Controlling Dimension: Millimeter.

Marking Codes

Part Number	WTDTC143T
Marking Code	03

Package Information

Qty：3k/Reel

CONTACT INFORMATION

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WAYON website: **http://www.way-on.com**
For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.